A WIDE BAND MULTIPORT PLANAR POWER DIVIDER DESIGN BY RADIALLY COMBINING MATCHED SECTORIAL COMPONENTS

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ABSTRACT

This paper proposes a new multiport planar power divider design by radially combining the sectorial components and the input and output matching networks. It can achieve good input match over a wide bandwidth without resorting to transformer sections of high impedance lines which are difficult to realize. This approach is applied to design 4-way and 14-way center fed power dividers in microstrip structures with good input match (VSWR < 1.5) over a bandwidth of 30 % and 15 %, respectively. A simple analysis method using the radial transmission line theory to model the microstrip sectorial components is presented to characterize the power divider. The calculated scattering parameters are found to be in good agreement with the measured data.

1 INTRODUCTION

Various designs of power dividers/combiners have been proposed in the literature, e.g., the survey paper [1]. One major problem associated with these power dividers is to achieve a good input match over a wide operation bandwidth. Theoretically, the dividers can include several transmission line sections of different impedances to provide good matching networks. The impedance levels will vary to a larger extent if either the number of output ports N or the desired bandwidth increases. In practice, it is hard to realize these impedance levels due to limitation of the achievable line width for microstrip or strip lines. For example, Hanczor and Kumar [2] proposed a 14-way power divider using maximally flat, two transformer sections. Their design called for two quarter wavelength sec-

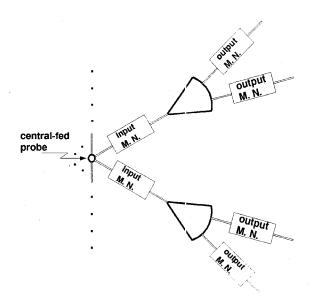


Fig.1: The whole topology of the proposed power divider design.

tions of characteristic impedances 37Ω and 140Ω . The line of the characteristic impedance 140Ω is not adequate for microstrip nor strip lines unless some special substrate is used.

2 DESCRIPTION FOR TOPOL-OGY

Fig. 1 shows the topology of the N-way power divider to be proposed in this paper. It consists of four parts: radial fan-out from the center-fed probe, input matching network, sectorial component, and output matching network. The sectorial

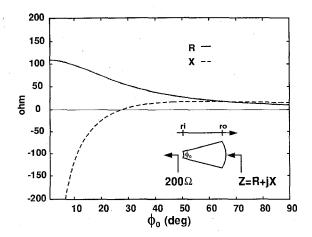


Fig.2: The impedance of the sectorial component of the 4-way power divider seen from the outer port versus the sector angle ϕ_0 at frequency $f_o = 3.3$ GHz. The outer radius r_o is 26.6 mm.

component may connect one input port to one output port and serve as an impedance transformer. It may even connect one input port to two or more output ports and provide additional power dividing stage. Note that the topology may reserve its pure symmetry if the number of output ports connected to each sectorial component is no more than two. The matching network is made of three transmission line sections of different impedance levels and different lengths.

3 EXPERIMENTAL AND THEO-RETICAL RESULTS

Two examples are included to demonstrate the design procedure. The power dividers are all designed and measured on a microstrip structure with the substrate dielectric constant 4.33 and height 1.5 mm. The output ports are 50Ω transmission lines which are realized by microstrip lines of width 2.89 mm.

A. 4-way power divider

The first example is the design of 4-way power divider in the microstrip environment. The sectorial component works like a tapered transmission line, thereby achieving an impedance transform from the central probe to the output port. In the present case, consider the center frequency

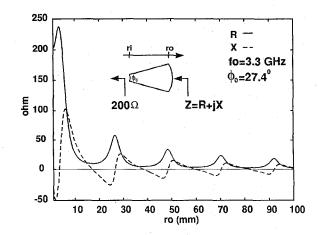


Fig.3: The impedance of the sectorial component of the 4-way power divider seen from the output port versus the outer radius r_o .

 $f_o = 3.3 \text{ GHz}$ and inner radius $r_i = 1.0 \text{ mm}$. From the requirement $J_1(kr_o) \cong 0$, the outer radius $r_o = 26.6$ mm is determined. By varying the sector angle ϕ_0 , Fig. 2 shows the impedance at the output port looking into the center probe which is of impedance $50 \cdot N = 200\Omega$. The imaginary part of the impedance is zero at $\phi_0 = 27.4^{\circ}$. The real part is about 58Ω , a good impedance match to the output port. A thus designed power divider has been fabricated and measured. It shows good VSWR at the center frequency, but of a rather narrow bandwidth. This occurrence can be accounted for Fig. 3 which exhibits a rapid impedance variation near the designed point of $r_o = 26.6$ mm. For this sake, it is hard to achieve good impedance match by using sectorial component over a wide frequency band.

A design procedure to improve the wide band match is proposed. Based on the topology shown in Fig. 1, it starts with the selection of adequate radius and angle of the sectorial component. For example, r_o is chosen to be 15 mm as evident from Fig. 3, where the impedance level of the sectorial component is comparatively flat. Although not shown here, the impedance curves versus r_o with different sector angles ϕ_0 have been investigated. It is found that a better impedance curve can be obtained by choosing a larger ϕ_0 , e.g., $\phi_0 = 51.4^{\circ}$. Given the geometric parameters, the generalized impedance matrix of the sectorial component can be extracted by radial transmission line theory [3]. Then, the impedance matrix is put into the

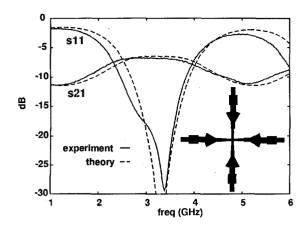


Fig.4: Calculated and measured S_{11} and S_{21} versus frequency for a 4-way power divider design with both input and output matching networks. The geometrical parameters on input matching section (from probe to sector): $W_1 = 1.07$, $L_1 = 5.64$, $W_2 = 1.17$, $L_2 = 3.31$, $W_3 = 2.89$, $L_3 = 5$ mm; on sectorial component: $\phi_0 = 51.4^0$, $r_i = 3.33$, $r_o = 15$ mm; on output matching section (from sector to output port): $W_3 = 4.96$, $L_3 = 5.94$, $W_2 = 6.97$, $L_2 = 5.89$, $W_1 = 2.89$, $L_1 = 10$ mm.

commercial software HP-EEsof to design the input and output matching networks for optimizing the bandwidth of the total topology.

Fig. 4 shows the scattering parameters versus frequency for the 4-way power divider with both input and output matching networks. It can be found that the theoretical and experimental results are in good agreement. The bandwidth of the power divider has been significantly enlarged by employing the present design procedure. Based on the criterion of VSWR < 1.5, the bandwidth is as large as 30.8 percent.

B. 14-way power divider

The second example considers the design of a 14-way power divider, for which it is more difficult to achieve good input match due to a large N, i.e., larger change of impedance level. According to the afore-mentioned design procedure, we select adequate outer radius $r_o = 15$ mm and sectorial angle $\phi_0 = 51.4^{\circ}$. The input and output matching sections are designed after an optimiza-

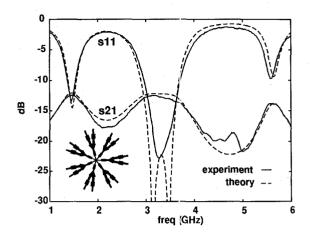


Fig.5: Calculated and measured S_{11} and S_{21} versus frequency for a 14-way power divider design with both input and output matching networks. The geometrical parameters on input matching section (from probe to sector): $W_1 = 0.50$, $L_1 = 14.0$, $W_2 = 4.36$, $L_2 = 12.4$, $W_3 = 2.89$, $L_3 = 5$ mm; on sectorial component: $\phi_0 = 51.4^0$, $r_i = 3.33$, $r_o = 15$ mm; on output matching section (from sector to output port): $W_3 = 4.80$, $L_3 = 7.35$, $W_2 = 8.19$, $L_2 = 7.84$, $W_1 = 2.89$, $L_1 = 10$ mm.

tion by using HP-EEsof. The scattering parameters of this 14-way power divider design are shown in Fig. 5. High bandwidth and good input match are achieved in the frequency band of interest. In the figure, both the theoretical and experimental results are shown and found to be in good agreement. For the criterion VSWR < 1.5, the divider exhibits good input match over a bandwidth of 15.7 percent centered at frequency 3.3 GHz.

For comparison sake, Hanczor and Kumar designed a 14-way power divider of similar performance [2]. Their design called for quarter-wave transformer sections with impedance level as large as 140 Ω . Such a high impedance line is quite difficult to realize in practical geometry if the substrate is present. The present design involves the matching transmission line sections of characteristic impedance at most 109 Ω which is easier for realization by most transmission line configurations, e.g., microstrip lines. The highest impedance level of the transmission lines in the matching sections can be further reduced by tuning other lines in

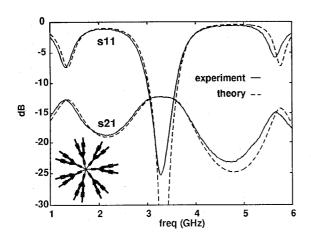


Fig.6: Another design for a 14-way power divider with both input and output matching networks. The geometrical parameters on input matching section (from probe to sector): $W_1 = 1.00$, $L_1 = 13.1$, $W_2 = 7.18$, $L_2 = 11.7$, $W_3 = 2.89$, $L_3 = 5$ mm; on sectorial component: $\phi_0 = 51.4^0$, $r_i = 3.33$, $r_o = 15$ mm; on output matching section (from sector to output port): $W_3 = 4.97$, $L_3 = 7.80$, $W_2 = 8.88$, $L_2 = 7.16$, $W_1 = 2.89$, $L_1 = 10$ mm.

the input and output matching networks. Shown in Fig. 6 are the scattering parameters of another design which has the highest impedance level of 85 Ω . The design can still achieve good input match, but with a slight decrease in bandwidth as the tradeoff.

4 CONCLUSIONS

A new topology is proposed to design a wide band multiport planar power divider. The achievable bandwidth is 30 % and 15 % for the 4-way and 14-way power dividers, respectively. Both the theoretical analysis and experimental measurement verify that the design based on the new topology can satisfy perfect balance of output powers and good input match over a wide bandwidth even when the number of output ports is large.

References

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