# Perfect Difference Codes for Synchronous Fiber-Optic CDMA Communication Systems

Chi-Shun Weng and Jingshown Wu

Abstract—In this paper, we introduce the perfect difference sets and propose a synchronous fiber-optic code-division multiple-access system (CDMA) using these sets to generate the signature codes. Two interesting properties of these codes are discussed: 1) any two different codes are cyclic-shifted with each other and 2) the cross correlation between any two different codes is exactly one. Thus, we may treat these codes as quasi-orthogonal codes. Using the first property of these codes, we can simplify design of the transmitter. In the receiver, we can use the second property to effectively eliminate the multiple-user interference (MUI) without reducing the number of usable codes. Based on the proposed transmitter and receiver, the system performance is derived. The numerical examples reveal that the proposed system can provide reliable communication even under heavy load. We believe that the proposed system using perfect difference codes outperforms any other synchronous fiber-optic CDMA systems.

*Index Terms*—Multiple-user interference (MUI), optical code division multiple access (CDMA), perfect difference set, spread spectrum.

## I. INTRODUCTION

**R**ECENTLY, fiber-optic code-division multiple-access (CDMA) systems have attracted much attention. Some techniques have been proposed [1]–[6]. Because synchronous optical CDMA (SOCDMA) systems are more efficient to utilize the bandwidth than any asynchronous optical CDMA systems, many studies of SOCDMA systems have been reported in the literature [7]–[12]. There are several possible ways to achieve slot synchronization. One way is that we add a synchronizer node located at the star coupler [13]. This node broadcasts a pulse called the sync pulse periodically. Using the periodic pulses, the synchronization may be achieved among all the transmitters.

One of the major concerns of designing a direct-detection SOCDMA system is the multiple-user interference (MUI) because the performance of this system is usually interference limited. That is, there is an asymptotic error floor no matter how much power the receiver received. Unlike in the electric domain, the signature sequence in a direct-detection SOCDMA consists of unipolar (0, 1), sequences. As a result, there is no strict orthogonal codes under the constraint that the total number of codes is equal to the code length.

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Publisher Item Identifier S 0733-8724(01)01097-0.

With this constraint, the modified prime sequence codes [5], which are popular for SOCDMA systems, provide some desirable characteristics. If two codes are cyclic-shifted with each other, then the cross correlation between these two codes is zero. We may say that the two codes are orthogonal with each other and are in the same group. If two codes are not in the same group, i.e., not cyclic-shifted with each other, then the cross correlation is one. There are some reports of using these codes as the signature codes for the SOCDMA systems to reduce the MUI [7]-[12]. Generally speaking, we may classify these SOCDMA systems using the modified prime sequence codes into two categories. One is that the total number of usable codes is equal to the code length  $p^2$ , where p is a prime [9]–[12]. The other is that the number of usable codes is less than  $p^2$  [7], [8]. The performances in the first category are still MUI constraint and the bit error probability can not be reduced arbitrarily by increasing received optical power. On the other hand, the systems in the second category preserve the last code sequence in each group to cancel the MUI. It is effective to reduce the MUI and the error floor can be easily eliminated. However, these systems have the maximal number of users  $p^2 - p$ instead of  $p^2$ .

Another important issue for the SOCDMA systems is the architectures of the encoder in the transmitter and the decoder in the receiver because they will affect the power budget and reconfiguration time of the systems [14]. In this paper, we study the "perfect difference sets" and propose a new SOCDMA system using these sets as the signature code sequences instead of modified prime sequences [15]. The perfect difference codes have the following interesting characteristics:

- 1) any two distinct codes are cyclic-shifted with each other;
- the cross correlation between any two distinct codes is exactly one.

However, the modified prime sequence codes have the cross correlation either one or zero. Although these codes are not strictly orthogonal, we may say that they are quasi-orthogonal. Using the first property, we propose a simple transmitter structure, in which the code sequence can be cyclic-shifted to get another transmitter. Using the second property, we also can easily design the receiver to remove the effect of the MUI. The system performance, with consideration of shot noise, thermal noise, avalanche photodiode (APD) bulk and surface leakage currents, is analytically formulated. The numerical examples show that the proposed system is very effective in eliminating the effect of the MUI. Moreover, the reliable communication ( $P_e \leq 10^{-9}$ ) of the proposed system is achievable even under heavy load.

The remainder of this paper is organized as follows. In Section II, we describe the perfect difference sets and the

Manuscript received June 22, 2000; revised October 2, 2000. This work was supported by the National Science Council and Ministry of Education, Taiwan, R.O.C., under Grants NSC89-2215-E-002-012 and 89-E-FA06-2-4.

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 TABLE I

 Some Example of the Perfect Difference Sets

k	v		perfect difference set												
3	7	0	1	3			_								
4	13	0	1	3	9										
5	21	0	1	4	<b>14</b>	16									
6	31	0	1	3	8	12	18								
8	57	0	1	3	13	32	36	43	52						
9	73	0	1	3	7	15	31	36	54	63					
10	91	0	1	3	9	27	49	56	61	77	81				
12	133	0	1	3	12	20	34	38	81	88	94	104	109		
14	183	0	1	3	16	23	<b>28</b>	42	76	82	86	119	137	154	175

sufficient condition to construct these sets. The characteristics of the perfect difference codes are also discussed. The feasible transmitter and receiver structures based on these codes are described in Section III. In Section IV, we analyze the system performance and present the bit error probability as functions of received optical power, noise power, code length, and the number of simultaneous users. Section V shows the numerical results and performance comparisons with other SOCDMA systems. Finally, we give the conclusion in Section VI.

## II. THE PERFECT DIFFERENCE SET

Definition: Let W be the v-set of the integers  $0, 1, \ldots, v-1$ modulo v. A set  $D = \{d_1, d_2, \ldots, d_k\}$  is a k-subset of W. For every  $a \neq 0 \pmod{v}$ , there are exactly  $\lambda$  ordered pairs  $(d_i, d_j), i \neq j$ , such that

$$d_i - d_j \equiv a \pmod{v}.$$
 (1)

A set D fulfilling these requirements is called a *perfect difference set* or  $(v, k, \lambda)$ -cyclic difference set modulo v. The vcyclic-shifted sets  $D_{\tau} = \{d_1 - \tau, d_2 - \tau, \dots, d_k - \tau\}$  modulo  $v, \tau = 0, 1, \dots, (v - 1)$ , all have the same property, where  $D_0 = D$  and each  $D_i \cap D_j$  for  $i \neq j$  is a  $\lambda$ -subset of W. Since there are k(k-1) pairs and the total number for  $a \neq 0 \pmod{v}$ is (v - 1), the relation among  $v, k, \lambda$  is

$$\lambda = \frac{k(k-1)}{v-1}.$$
(2)

Because of the rigid property, arbitrary values of v, k, and  $\lambda$  do not in general yield a perfect difference set. However, a special type of perfect difference sets is the  $(v, k, \lambda = 1)$ -cyclic difference set. The existence of the  $(v, k = q+1, \lambda = 1)$ -cyclic difference set, where q is any power of any prime, has been proved and constructed by Singer [15]. Because it has a good property with minimum number of overlap,  $\lambda = 1$ , between two cyclic-shifted sets, we take the advantage of this cyclic difference set to construct the signature codes. For brevity, we name such set as *cyclic difference set* through this paper. More detailed information about the perfect difference sets can be found in [16] and [17]. Some cyclic difference sets are presented in Table I. Notice that there is not only one set for each k = q + 1 [15]. Actually, the number of distinct cyclic difference sets, which are not cyclic-shifted with one another, for each

k = q + 1 is even, since each cyclic difference set can be paired with its inverse.

We can construct v perfect difference codes  $C_{\tau} = \{c_{\tau,0}, c_{\tau,1}, \ldots, c_{\tau,i}, \ldots, c_{\tau,(v-1)}\}, \tau \in W$ , based on the cyclic difference set with the rule

$$c_{\tau,i} = \begin{cases} 1, & \text{for } i \in D_{\tau} \\ 0, & \text{otherwise.} \end{cases}$$
(3)

The code weight, code length, and total number of codes are k, v, and v, respectively, where  $v = k^2 - k + 1$ . An example for k = 5 is presented in Table II. From the property of the cyclic difference set, the correlation  $\Phi_{x,y}$  between any two codes  $C_x$  and  $C_y$  can be expressed as

$$\Phi_{x,y} = \begin{cases} k, & \text{if } x = y\\ 1, & \text{if } x \neq y. \end{cases}$$
(4)

Although these perfect difference codes are not strictly orthogonal codes, we may say that they are quasi-orthogonal codes because of the unity cross correlation between two distinct codes. Notice that the correlation in (4) is the same as that in the extended modified prime sequence codes [18]. However, the cyclic-shifted property of perfect difference codes makes the design of a tunable transmitter or a tunable receiver simpler.

#### **III. SYSTEM DESCRIPTION**

## A. Transmitter Structure

For each subscriber, it is assigned a unique code sequence. When User x wants to send a bit to User y over a bit duration  $T_b$ , it sends k optical pulses correspondent to User y's code sequence if the data bit is 1, otherwise, no pulses are sent. Using the characteristic of the cyclic difference codes, we propose an all-optical tunable SOCDMA transmitter structure as shown in Fig. 1. The input optical pulse is split into k branches with equal power. In the *i*th branch, there is a fixed optical delay line with delay  $d_i \cdot T_c$ , where  $d_i \in D_0$  and  $T_c$  represent a chip duration given by

$$T_c = \frac{T_b}{v}.$$
 (5)

Thus, if there is no any relative delay in the following tunable delay lines, the output optical pulses are correspondent with the first code  $C_0$ . In order to cyclic-shift  $C_0$  to form another code  $C_j$ , the delay selector between the splitter and combiner, which

$\overline{j}$		Cy	clic I	Differ	rence	Sets			Perfect Difference Codes			
0	$D_0$	={	0	1	4	14	16	}	$C_0$	= 11001000000001010000		
1	$D_1$	={	20	0	3	13	15	}	$C_1$	= 10010000000010100001		
2	$D_2$	={	19	20	2	12	14	}	$C_2$	=00100000000101000011		
3	$D_3$	={	18	19	1	11	13	}	$C_3$	= 01000000001010000110		
4	$D_4$	={	17	18	0	10	12	}	$C_4$	= 100000000010100001100		
5	$D_5$	={	16	17	20	9	11	}	$C_5$	= 000000000101000011001		
6	$D_6$	={	15	16	19	8	10	}	$C_6$	= 000000001010000110010		
7	$D_7$	={	14	15	18	7	9	}	$C_7$	= 000000010100001100100		
8	$D_8$	={	13	14	17	6	8	}	$C_8$	= 000000101000011001000		
9	$D_9$	={	12	13	16	5	7	}	$C_9$	= 000001010000110010000		
10	$D_{10}$	={	11	12	15	4	6	}	$C_{10}$	= 000010100001100100000		
11	$D_{11}$	={	10	11	14	3	5	}	$C_{11}$	= 000101000011001000000		
12	$D_{12}$	={	9	10	13	<b>2</b>	<b>4</b>	}	$C_{12}$	=001010000110010000000		
13	$D_{13}$	={	8	9	12	1	3	}	$C_{13}$	= 010100001100100000000		
14	$D_{14}$	={	7	8	11	0	2	}	$C_{14}$	= 101000011001000000000		
15	$D_{15}$	={	6	7	10	20	1	}	$C_{15}$	= 01000011001000000001		
16	$D_{16}$	={	<b>5</b>	6	9	19	0	}	$C_{16}$	= 10000110010000000010		
17	$D_{17}$	={	4	5	8	18	20	}	$C_{17}$	= 00001100100000000101		
18	$D_{18}$	={	3	4	7	17	19	}	$C_{18}$	= 00011001000000001010		
19	$D_{19}$	={	2	3	6	16	18	}	$C_{19}$	= 00110010000000010100		
20	$D_{20}$	={	1	2	5	15	17	}	$C_{20}$	= 01100100000000101000		

TABLE II THE PERFECT DIFFERENCE CODES FOR k = 5



Fig. 1. Tunable transmitter structure.



Fig. 2. Delay line selector in the *i*th branch.

contains two 2 × 2 optical switches and optical delay lines as shown in Fig. 2, is needed. The delay in the lower optical delay line is a bit duration longer than that in the upper one. When the two switches are both set in the bar state by  $s_i^j = 0$ , then the input pulse is no relative delay. If they are in the cross state by  $s_i^j = 1$ , then one bit delay is introduced. The  $s_i^j$ 's are controlled by the delay controller and can be expressed as

$$s_i^j = \begin{cases} 1, & \text{if } j > d_i \\ 0, & \text{otherwise.} \end{cases}$$
(6)

Therefore, the output k pulses of the optical power combiner are correspondent to code  $C_i$ . Although the output pulses match to the code  $C_i$ , it is not the desired pattern because of losing synchronization with other users after the delay selectors. Hence, we need tunable optical delay lines, whose structure is shown in Fig. 3 [19], to delay  $(v - j) \cdot T_c$  to restore the synchronization. For the sake of delay time from 0 to v - 1 chip duration, there are  $\lceil \log_2 v \rceil + 1$  stages of  $2 \times 2$  switches, where  $\lceil t \rceil$  denotes the smallest integer not less than t. And the ith stage may introduce  $2^{\lceil \log_2 v \rceil - i} \cdot T_c$  delay,  $i \in \{1, 2, \dots, \lceil \log_2 v \rceil\}$ . If it needs to delay  $m \cdot T_c$ , the delay controller selects control sequence  $a_1^m a_2^m \dots a_{\lceil \log_2 v \rceil + 1}^m$  appropriately to control the  $\lceil \log_2 v \rceil + 1$ stages such that the total delay is equal to  $m \cdot T_c$ . The method to select the control sequence is as follows. First, it converts minto  $\lceil \log_2 v \rceil$  binary sequence  $b_1^m b_2^m \dots b_{\lceil \log_2 v \rceil}^m$  and pads two zero bits  $b_0^m = 0$  and  $b_{\lceil \log_2 v \rceil + 1}^m = 0$  before and after the binary sequence, respectively, to form the new binary sequence  $b_0^m b_1^m b_2^m \dots b_{\lceil \log_2 v \rceil}^m b_{\lceil \log_2 v \rceil+1}^m$ . Then, let

$$a_i^m = b_{i-1}^m \oplus b_i^m, \quad \text{for } i = 1, 2, \dots, \lceil \log_2 v \rceil + 1$$
 (7)

where  $\oplus$  means the Exclusive-OR operation and the control sequence  $a_1^m a_2^m \dots a_{\lceil \log_2 v \rceil + 1}^m$  is the desired one. Some examples of k = 5 are presented in Table III. For suitable design, the subnanosecond reconfiguration time of the tunable optical delay lines may be feasible [19].

Another important concern on designing a SOCDMA system is the beating of the optical carriers. If the lasers from different transmitters simultaneously emit on the same wavelength it may destroy the signals in all channels. To reduce this effect, we may use a broadband light source or a polorization scrambler at the end of a transmitter to reduce the beating [20].



Fig. 3. Tunable optical delay lines.

#### B. Receiver Structure

As well as the transmitter, we can use the properties of the cyclic difference codes to structure a simple receiver. Unlike the tunable transmitter, the receiver is fixed in the proposed system as shown in Fig. 4. If it needs a tunable receiver, we also can apply the design principle of the tunable transmitter mentioned. Similar to the techniques proposed in [7]-[10], we use the second branch to eliminate the MUI. The received signal power per pulse, P, is split into two branches with unequal power,  $P_1$  and  $P_2$  with respective to the first and the second branches. The first branch has an optical correlator, an APD, an integrator, and a sampler. The optical correlator contains k tap delay lines correspondent to the desired code sequence such that the k chips are delayed to the last chip duration. The correlator output is then photodetected, integrated over the last chip duration, and sampled at the end of the bit duration. The output  $Y_1$  of the sampler contains the desired signal, interference from other users, and noise. The second branch has an APD, an integrator, a sampler, and a divider. After integrated over a bit duration and sampled, the output signal  $Y_2$  is then divided by r, where r is a constant and its optimal value only depends on the code weight k. The relation between r and k will be described and the performances with and without a divider will be presented later. After subtracting  $Y_2/r$  from  $Y_1$ , the output signal Y is then fed into the ON–OFF keying (OOK) demodulator. If Y is less than the constant threshold value of the OOK demodulator the output bit is 0, otherwise is 1.

#### **IV. PERFORMANCE ANALYSIS**

We assume that the number of simultaneous (active) users is N. To express the state of code  $C_j$ ,  $j \in W$ , we define a random variable  $z_j$  as

$$z_j = \begin{cases} 1, & \text{if code } C_j \text{ belongs to an active user} \\ 0, & \text{otherwise.} \end{cases}$$
(8)

Thus, the number of users who sent the data bit 1 within a bit time can be written as

$$N_1 = \sum_{j=0}^{\nu-1} b_j z_j \tag{9}$$

where  $b_j \in \{0, 1\}$  denotes the binary data bit. Notice that  $N_1$  is a random variable. Without loss of generality, we assume the first user with the assigned code  $C_0$  is the desired user and  $b_0$  is the desired data bit.

The average photon arrival rate  $\lambda_1$  per pulse at the input of the optical correlator in the first branch is given by

$$\lambda_1 = \frac{\eta P_1}{hf} \tag{10}$$

TABLE IIITHE CONTROL SEQUENCE FOR k = 5

TI	1.1	him a way hida	control cocuron co
User	delay	binary bits	control sequence
j	m	$b_0^m b_1^m b_2^m b_3^m b_4^m b_5^m b_6^m$	$a_1^m a_2^m a_3^m a_4^m a_5^m a_6^m$
0	0	0000000	000000
1	20	0101000	111100
3	17	0100010	110011
20	1	0000010	000011

where

- $\eta$  APD quantum efficiency;
- *h* Plank's constant;
- f optical frequency.

Given  $N_1$  and the desired bit  $b_0 = 1$ , the conditional probability density function of the output  $Y_1$  after the sampler in the first branch can be expressed as [6]

$$P_{Y_1}(y_1|N_1, b_0 = 1) = \frac{1}{\sqrt{2\pi\sigma_{y_1}^2}} e^{-(y_1 - \mu_{y_1})^2/2\sigma_{y_1}^2}$$
(11)

where the mean of  $Y_1$ ,  $\mu_{y_1}$ , can be expressed as

$$\mu_{y_1} = GT_c \left[ \left( \frac{k + N_1 - 1}{k} \right) \lambda_1 + I_b / e \right] + T_c I_s / e. \quad (12)$$

Here, G is the average APD gain, e is the electron charge,  $I_b/e$  is the contribution of the APD bulk leakage current to the APD output,  $I_s$  is the APD surface leakage current, and the variance of  $Y_1$ ,  $\sigma_{y_1}^2$ , can be written as

$$\sigma_{y_1}^2 = G^2 F_e T_c \left[ \left( \frac{k + N_1 - 1}{k} \right) \lambda_1 + I_b / e \right] + T_c I_s / e + \sigma_{th}^2$$
(13)

where  $\sigma_{th}^2$  is the variance of thermal noise and  $F_e$  is the excess noise factor given by

$$F_e = k_{\text{eff}}G + (2 - 1/G)(1 - k_{\text{eff}}).$$
(14)

Here,  $k_{\text{eff}}$  is the APD effective ionization ratio.  $\sigma_{th}^2$  is given by

$$\sigma_{th}^2 = 2k_B T_r T_c / (e^2 R_L) \tag{15}$$

where

 $k_B$  Boltzmann's constant;

 $T_r$  receiver noise temperature;

 $R_L$  receiver load resistance.

Similarly, given  $N_1$  and  $b_0 = 0$ , the conditional probability density function of  $Y_1$  is

$$P_{Y_1}(y_1|N_1, b_0 = 0) = \frac{1}{\sqrt{2\pi\sigma'_{y_1}^2}} e^{-(y_1 - \mu'_{y_1})^2/2\sigma'_{y_1}^2}$$
(16)

where the mean of  $Y_1$ ,  $\mu'_{y_1}$ , can be expressed as

$$\mu_{y_1}' = GT_c \left[ \left( \frac{N_1}{k} \right) \lambda_1 + I_b / e \right] + T_c I_s / e \tag{17}$$

and the variance of  $Y_1, \sigma'^2_{y_1}$ , can be written as

$$\sigma'_{y_1}^2 = G^2 F_e T_c \left[ \left( \frac{N_1}{k} \right) \lambda_1 + I_b / e \right] + T_c I_s / e + \sigma_{th}^2.$$
(18)



Fig. 4. The fixed receiver structure.

TABLE IV
LINK PARAMETERS

Name	Symbol	Value
Light wavelength		$1.3 \mu m$
APD	$\eta$	0.6
Quantum efficiency		
APD gain	G	100
APD effective	$k_{eff}$	0.02
ionization ratio		
APD bulk	$I_b$	0.1nA
leakage current		
APD surface	$I_s$	10nA
leakage current		
chip duration	$T_c$	$0.1 \mathrm{ns}$
bit rate	$\frac{1}{-} = \frac{1}{-}$	
	$T_b = vT_c$	
Receiver noise	$T_r$	300k
temperature		
Receiver load resistor	$R_L$	$1030\Omega$

In the second branch, the average photon arrival rate  $\lambda_2$  per pulse at the input of the APD is given by

$$\lambda_2 = \frac{\eta P_2}{hf}.\tag{19}$$

Given  $N_1$ , the conditional probability density function of the output  $Y_2$  after the sampler in the second branch can be expressed as

$$P_{Y_2}(y_2|N_1) = \frac{1}{\sqrt{2\pi\sigma_{y_2}^2}} e^{-(y_2 - \mu_{y_2})^2/2\sigma_{y_2}^2}$$
(20)

where the mean of  $Y_2$ ,  $\mu_{y_2}$ , can be expressed as

$$\mu_{y_2} = GT_c \left[ N_1 k \lambda_2 + v I_b / e \right] + v T_c I_s / e \tag{21}$$

and the variance  $\sigma_{y_2}^2$  is given by

$$\sigma_{y_2}^2 = G^2 F_e T_c \left[ N_1 k \lambda_2 + v I_b / e \right] + v (T_c I_s / e + \sigma_{th}^2) \quad (22)$$

respectively.

After subtracting the second signal  $Y_2/r$  from the first signal  $Y_1$ , we obtain the result signal at the input of the OOK demodulator as

$$Y = Y_1 - \frac{1}{r} Y_2. \tag{23} \text{ respect}$$

The mean of Y is

$$E(Y) = \begin{cases} GT_c \left[ \left( \frac{k + N_1 - 1}{k} \right) \lambda_1 - \frac{N_1 k \lambda_2}{r} + \left( 1 - \frac{v}{r} \right) I_b / e \right] \\ + \left( 1 - \frac{v}{r} \right) T_c I_s / e, & \text{if } b_0 = 1 \\ GT_c \left[ \left( \frac{N_1}{k} \right) \lambda_1 - \frac{N_1 k \lambda_2}{r} + \left( 1 - \frac{v}{r} \right) I_b / e \right] \\ + \left( 1 - \frac{v}{r} \right) T_c I_s / e, & \text{if } b_0 = 0. \end{cases}$$

$$(24)$$

To cancel the MUI, we set

 $\frac{N_1\lambda_1}{k} = \frac{N_1k\lambda_2}{r}.$ (25)

That is,

$$P_{1} = \frac{k^{2}}{r} P_{2}$$
$$= \frac{k^{2}/r}{k^{2}/r+1} P.$$
 (26)

Therefore, the mean and variance of Y are

$$E(Y) = \begin{cases} GT_c \left[ \left( \frac{k-1}{k} \right) \lambda_1 + \left( 1 - \frac{v}{r} \right) I_b / e \right] \\ + \left( 1 - \frac{v}{r} \right) T_c I_s / e, & \text{if } b_0 = 1 \\ GT_c \left[ \left( 1 - \frac{v}{r} \right) I_b / e \right] + \left( 1 - \frac{v}{r} \right) T_c I_s / e, & \text{if } b_0 = 0 \end{cases}$$

(27)

and

$$\operatorname{Var}(Y) = \begin{cases} \sigma_Y^2 = \sigma_{y_1}^2 + \frac{1}{r^2} \sigma_{y_2}^2, & \text{if } b_0 = 1\\ \sigma_{Y}^{\prime 2} = \sigma_{y_1}^{\prime 2} + \frac{1}{r^2} \sigma_{y_2}^2, & \text{if } b_0 = 0 \end{cases}$$
(28)

tively.

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Hence, the constant threshold value of the OOK demodulator is set as

$$\theta = GT_c \left[ \left( \frac{k-1}{2k} \right) \lambda_1 + \left( 1 - \frac{v}{r} \right) I_b / e \right] + \left( 1 - \frac{v}{r} \right) T_c I_s / e.$$
(29)

The bit error probability given the number of simultaneous users N = n can be derived as follows:

$$P_{r}(\text{bit error}|N = n)$$

$$= P_{r}(Y \ge \theta | N = n, b_{0} = 0) \cdot P_{r}(b_{0} = 0)$$

$$+ P_{r}(Y < \theta | N = n, b_{0} = 1) \cdot P_{r}(b_{0} = 1)$$

$$= \sum_{m=0}^{n-1} P_{r}(Y \ge \theta | N_{1} = m, b_{0} = 0)$$

$$\cdot P_{r}(N_{1} = m | N = n, b_{0} = 0) \cdot P_{r}(b_{0} = 0)$$

$$+ \sum_{m=1}^{n} P_{r}(Y < \theta | N_{1} = m, b_{0} = 1)$$

$$\cdot P_{r}(N_{1} = m | N = n, b_{0} = 1) \cdot P_{r}(b_{0} = 1)$$

where

$$P_{r}(Y \ge \theta | N_{1} = m, b_{0} = 0) = \frac{1}{2} \operatorname{erfc} \left( \frac{GT_{c} \left( \frac{k-1}{2k} \right) \lambda_{1}}{\sqrt{2\sigma'_{Y}^{2}}} \right)$$

$$(30)$$

$$P_{r}(Y < \theta | N_{1} = m, b_{0} = 1) = \frac{1}{2} \operatorname{erfc} \left( \frac{GT_{c} \left( \frac{k-1}{2k} \right) \lambda_{1}}{\sqrt{2\sigma_{Y}^{2}}} \right)$$

$$(31)$$

$$P_r(N_1 = m | N = n, b_0 = 0) = \binom{n-1}{m} \cdot \frac{1}{2^{n-1}}$$
(32)

$$P_r(N_1 = m | N = n, b_0 = 1) = {\binom{n-1}{m-1}} \cdot \frac{1}{2^{n-1}}.$$
 (33)

Here,  $\operatorname{erfc}(\cdot)$  stands for the complementary error function, defined as

$$\operatorname{erfc}(z) = \frac{2}{\sqrt{\pi}} \int_{z}^{\infty} \exp(-u^2) du.$$
 (34)

and it is assumed that  $P_r(b_0 = 0) = P_r(b_0 = 1) = 1/2$ .

To analyze the optimal value of r, we take the maximum noise into consideration. In other words, the number of users who sent data bit 1 is the same as the maximum number of subscribers, that is,  $N_1 = v$ . In this case, we assume that the shot noise is dominant and other noise is negligible. Hence, the variance of Y becomes

$$\operatorname{Var}(Y) = \sigma_{y_1}^2 + \frac{1}{r^2} \sigma_{y_2}^2$$
$$= G^2 F_e T_c \left(k + \frac{v}{kr}\right) \lambda_1. \tag{35}$$

The effective SNR(r) can be written as

$$SNR(r) = \frac{\left[GT_c\left(\frac{k-1}{k}\right)\lambda_1\right]^2}{G^2 F_e T_c\left(k+\frac{v}{kr}\right)\lambda_1}$$
$$= \left(\frac{k^2/r}{k^2/r+1}\right) \cdot \frac{1}{\left(k+\frac{v}{kr}\right)} \cdot \frac{T_c\lambda}{F_e} \cdot \left(\frac{k-1}{k}\right)^2$$
$$= \Psi(r) \cdot \frac{T_c\lambda}{F_e} \cdot \left(\frac{k-1}{k}\right)^2$$
(36)

where

$$\Psi(r) = \left(\frac{k^2/r}{k^2/r+1}\right) \cdot \frac{1}{\left(k+\frac{v}{kr}\right)}$$
(37)

and  $\lambda$  is the average photon arrival rate per pulse before the splitter and is given by

$$\lambda = \frac{\eta P}{hf}.$$
(38)

Maximizing SNR(r) is the same as maximizing the  $\Psi(r)$ . And the maximum value of  $\Psi(r)$  is obtained under

$$r = \sqrt{k^2 - k + 1}.\tag{39}$$

## V. NUMERICAL RESULTS

In this section, we present the numerical results of the systems using the perfect difference codes. Since the performances of the two SOCDMA systems [8], [7] (which is the same as Canceler 1 in [8]) are not MUI constraint, we compare here the performances of three SOCDMA systems: 1) the system with interference estimator [8]; 2) the proposed system without a divider; and 3) the proposed system with an optimal divider. Because the Canceler 2 has the best performance in the systems with interference estimators [8], we only take the Canceler 2 into consideration. Furthermore, we assume that there is no error on estimating the interference in that system. Thus, the performances of the systems with interference estimators are the lower bound. Some common parameters and their values are listed in Table IV. Other parameter values yield similar results.

The relation between  $\Psi(r)$  and r for various values of k is shown in Fig. 5. The optimal value of r is around 12. For k = 17, the value of  $P_1$  is almost equal to 0.95P according to (26). In other words, the most received power is assigned to the first branch. On the other hand, if we do not use a divider in the second branch to optimize the performance, i.e., r = 1, the value of  $P_1$  is almost equal to P. It seems that the performance of the system without the divider is better than that using a divider due to a little larger power in the first branch. However, the noise power of Y in the system without a divider is much larger than that using a divider due to that the noise power contributed from the second branch of the former system is much larger than that of the latter one according to (28). As a result, the performance of the system without a divider is about 2.42 dB worse than that of the system using an optimal divider. The

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Fig. 5. Relation between  $\Psi(r)$  and r.

figure also shows that the curves come down with the value of k increases due to the raise of shot noise.

Fig. 6 shows the performances of three SOCDMA systems under the same received power per pulse  $P = 10 \ \mu$ W. Firstly, we compare the performances between our proposed systems with and without an optimal divider. The same as discussed above, the performance of the proposed system without a divider is much worse than that of the proposed system using an optimal divider. On the other hand, when the number of simultaneous users increases the performances are getting worse due to the increment of shot noise. Moreover, the bit error probability is smaller for larger code weight k under the same number of simultaneous users. This is because that each undesired user who sends the data bit 1 contributed 1/k optical pulse into the last chip duration at the output of optical correlator. And it also induces the correspondent shot noise after the photodetector. Therefore, the larger code weight results in the smaller shot noise and bit error probability. Secondly, we compare the performances between the systems with interference estimators and the proposed systems with an optimal divider. Because the code length for the modified prime sequence codes is always the square of a prime there is no equal code length between any modified prime sequence codes and perfect difference codes. Thus, we compare the performance between the code weights p = 11 (code length = 121) and p = 13 (code length = 169) for modified prime sequence codes and the code weight k = 12(code length = 133) and k = 14 (code length = 183) for perfect difference codes. The numerical results show that the performances of the systems using interference estimators under code weight p = 11 and p = 13 are worse than those of the proposed systems with an optimal divider under code weight k = 12 and k = 14. By the way, the performance of the former systems is also worse than that of the latter system under the same code weight p = 17 (code length = 289) or k = 17 (code length = 273). This is because that the power in the first branch of the proposed systems is almost two times larger than that of the systems with interference estimators. Moreover, the noise power contributed from the second branch of the proposed system is suppressed by the optimal divider. As a result, the effective SNR of the proposed system with an optimal divider is larger than that of the systems with interference estimators. Note that we do not need to keep any code to estimate the interference of the proposed system but the last code in each group is never assigned to any user and is reserved for the MUI estimation at the receiving end of the systems with interference estimators [8]. Thus, the proposed systems are more efficient to utilize the codes than the systems with interference estimators.

Under the maximum number of simultaneous users N = vin the proposed systems or  $N = p^2 - p$  in the systems with interference estimators, the bit error probability versus the received power P is shown in Fig. 7. For the received power is 16  $\mu$ W and  $k \leq 17$ , the bit error probability of the system with the optimal divider is smaller than  $10^{-9}$ . The results also show that the performance of the proposed systems without a divider or the systems with interference estimators is worse than that of the proposed systems with the optimal divider. Moreover, the



Fig. 6. Bit error probability comparisons under the same received power per pulse  $P = 10 \ \mu$ W.



Fig. 7. Bit error probability comparisons under different code weights and full load.

larger the code weight has, the worse the performance is due to more shot noise. Hence, it reveals that the bit error probability is going down with the increment of received power. In other words, the error floor can be completely removed and the performance of the system is no longer interference limited.

## VI. CONCLUSION

The application of the perfect difference sets in the SOCDMA systems is investigated first time. We also propose a new SOCDMA system using these sets as the signature

codes. Based on the cyclic-shifted and unity cross correlation properties of these codes, we propose the transmitter and receiver structures. An optimal divider is used on the receiver to enhance the system performance. The bit error probability is derived analytically. The numerical results, with consideration of shot noise, thermal noise, APD bulk, and surface leakage currents, show that it is effective to eliminate the MUI and the asymptotic error floor which appears in the SOCDMA systems with modified prime sequence codes. Moreover, the proposed system can provide reliable communication even under heavy load without reducing the number of usable codes. It is believed that the proposed system performs better than other SOCDMA systems.

### ACKNOWLEDGMENT

The authors would like to thank the anonymous reviewers for valuable suggestions.

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