

Characterization of the Ultrathin HfO₂ and Hf-Silicate Films Grown by Atomic Layer Deposition

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Abstract—The physical properties of HfO₂ and Hf-silicate layers grown by the atomic layer chemical vapor deposition are characterized as a function of the Hf concentration and the annealing temperature. The peaks of Fourier transform infrared spectra at 960, 900, and 820 cm⁻¹ originate from Hf–O–Si chemical bonds, revealing that a Hf-silicate interfacial layer began to form at the HfO₂/SiO₂ interface after postdeposition annealing process at 600 °C for 1 min. Moreover, the intensity of the peak at 750 cm⁻¹ can indicate the degree of crystallization of HfO₂. The formed Hf-silicate layer between HfO₂ and SiO₂ is also confirmed by X-ray photoelectron spectroscopy.

Index Terms—Fourier transform infrared (FTIR), HfO₂, Hf-silicate, high-*k*, X-ray photoelectron spectroscopy (XPS), X-ray reflectivity (XRR).

I. INTRODUCTION

IN ORDER TO meet the International Technology Roadmap for Semiconductor [1] requirements for equivalent oxide thickness (EOT) and gate leakage current, the conventional SiO₂ gate dielectric might need to be replaced by higher dielectric constant materials [2]. Using the “high-*k* dielectric” allows the device to maintain an EOT of 1 nm or less, while having a larger physical thickness to reduce the tunneling current. Among the various high-*k* dielectric materials, hafnium-based dielectrics are being widely investigated due to their high dielectric permittivity, high melting point, and acceptable thermal and chemical stabilities [3]–[6]. However, the hafnium-based dielectrics still have some critical issues, such

Manuscript received May 8, 2006; revised December 19, 2006. The work of the National Taiwan University group was supported by the National Science Council, Taiwan, R.O.C., under Contract 93-2215-E-002-003 and Contract 93-2215-E-002-017. The review of this paper was arranged by Editor G. Groeseneken.

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Digital Object Identifier 10.1109/TED.2007.892012

as mobility degradation [7] and large flatband voltage shift [8], which impede their industrial application. Furthermore, it is hard to maintain the amorphous state for HfO₂ due to its low crystallization temperature (300 °C–400 °C) [9]. The pseudobinary materials, such as Hf-aluminate and Hf-silicate, combine the advantages of acceptable dielectric constant and desirable higher crystallization temperature [10]. Therefore, the material change of high-*k* dielectrics after the thermal process is a significant issue for the future CMOS applications. In this paper, we investigate the material and electrical modification of Hf-based dielectrics at different annealing conditions and different Si incorporations.

II. EXPERIMENT

The high-*k* films were on the 15–25 Ω · cm p-type Si wafers. All samples were treated with an SC-1 preclean process before high-*k* dielectrics deposition. This cleaning process typically results in a chemical oxide layer between high-*k* dielectrics and the Si substrate to enhance initial growth of the high-*k* dielectrics. The HfO₂ and Hf-silicate films were deposited by hot-wall atomic layer chemical vapor deposition [11] with a reaction temperature of 300 °C, using alternating surface-saturation reactions of HfCl₄, SiCl₄, and H₂O. Note that Hf-silicate films with different Hf/Si ratios (Hf_{0.38}Si_{0.62}O₂, Hf_{0.5}Si_{0.5}O₂, and Hf_{0.66}Si_{0.34}O₂) were grown using alternated nanomixed HfO₂/SiO₂ [12]. After deposition, a silicon nitride cap to form a thin passivation layer (< 0.5 nm) was deposited at 600 °C for 10 s using NH₃ and SiCl₄ precursors. The samples were annealed in the furnace at 600 °C for 1 min and 1000 °C for 5 min in N₂ ambient for postdeposition annealing (PDA). The furnace was kept at the atmospheric pressure and purged by nitrogen. The electrical properties were studied using metal–insulator–semiconductor (MIS) capacitors with a 50-nm-thick Pt electrode deposited by E-gun and a circular area of 2.5 × 10⁻⁴ cm² defined by the shadow mask. Another Al contact is deposited on the back of the sample. The postmetalization annealing (PMA) was done in forming gas, N₂(90%) + H₂(10%), at 400 °C for 30 min before the electrical measurement. Fourier transform infrared (FTIR) spectroscopy was used to observe transmission spectra of the different chemical bonds and the change of bonding state after thermal processing. Each spectrum was measured with a resolution of 4 cm⁻¹ and a range of 370–1800 cm⁻¹ and transformed from transmission

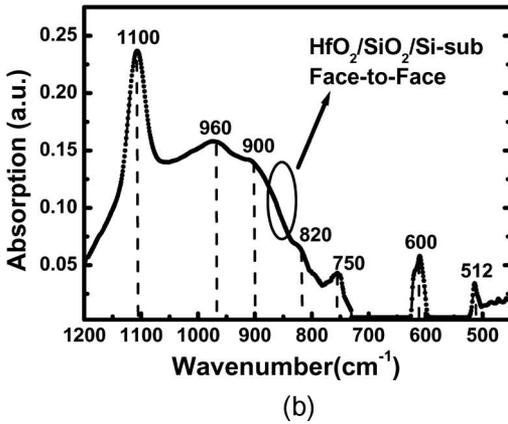
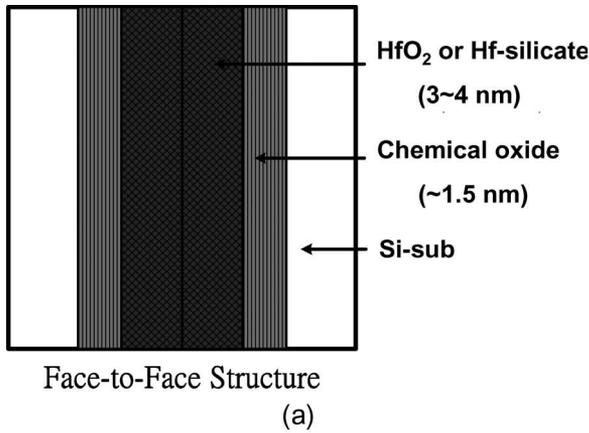


Fig. 1. (a) Scheme of FTIR measurement structure. (b) FTIR spectra of HfO₂ after PDA at 600 °C for 1 min in flowing N₂ measured by “face-to-face” structure. The intensity of IR signal with ultrathin high-*k* layer can be enhanced by the “face-to-face” method.

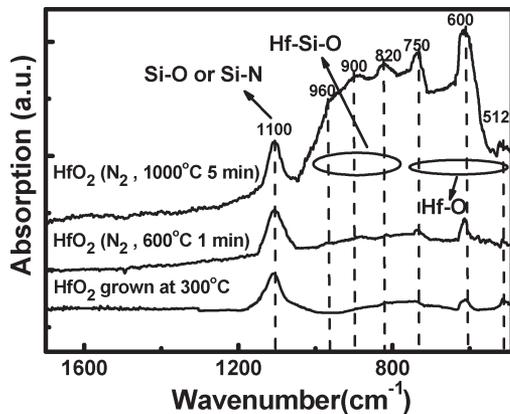


Fig. 2. FTIR spectra of ultrathin HfO₂ films as-deposited, annealed at 600 °C for 1 min, and annealed at 1000 °C for 5 min in N₂ ambient. The positions of the peaks in the three curves are indicated by dashed lines. The intensity of the peak at 750 cm⁻¹ is clearly seen to increase the thermal budget.

to absorption. High-resolution transmission electron microscope (HRTEM) and X-ray photoelectron spectroscopy (XPS) were used for physical thickness inspection and observation of the chemical composition of the gate dielectrics. X-ray reflectivity (XRR) using *CuKα* source was used to study the constituent thickness and roughness of these films. The thickness obtained by XRR was calibrated with HRTEM measure-

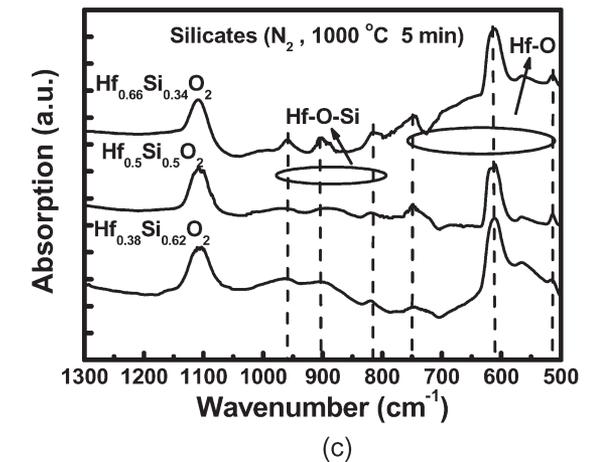
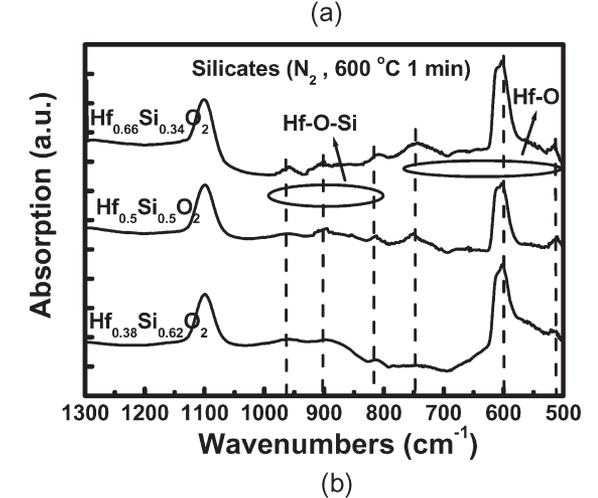
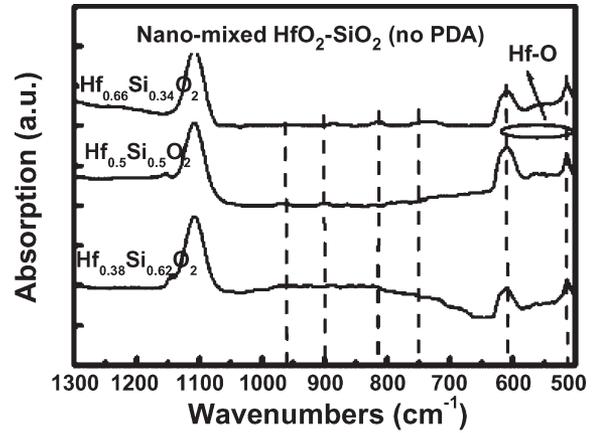


Fig. 3. FTIR absorption spectra of Hf-silicate. The samples in (a) were not annealed, and in (b) and (c), the samples were annealed in N₂ ambient for 1 min at 600 °C and for 5 min at 1000 °C, respectively. There are only two peaks (at 600 and 512 cm⁻¹) in the as-deposited nanomixed HfO₂/SiO₂, indicating no Hf-silicate formation.

ments for layer thickness and atomic force microscopy (AFM) for surface roughness. The electrical properties of HfO₂ and Hf-silicate films on chemical oxide with different PDA conditions were also studied. The capacitance–voltage (*C–V*) and current–voltage (*I–V*) characteristics for electrical properties were measured with HP 4284A and Keithley 4200 instruments, respectively.

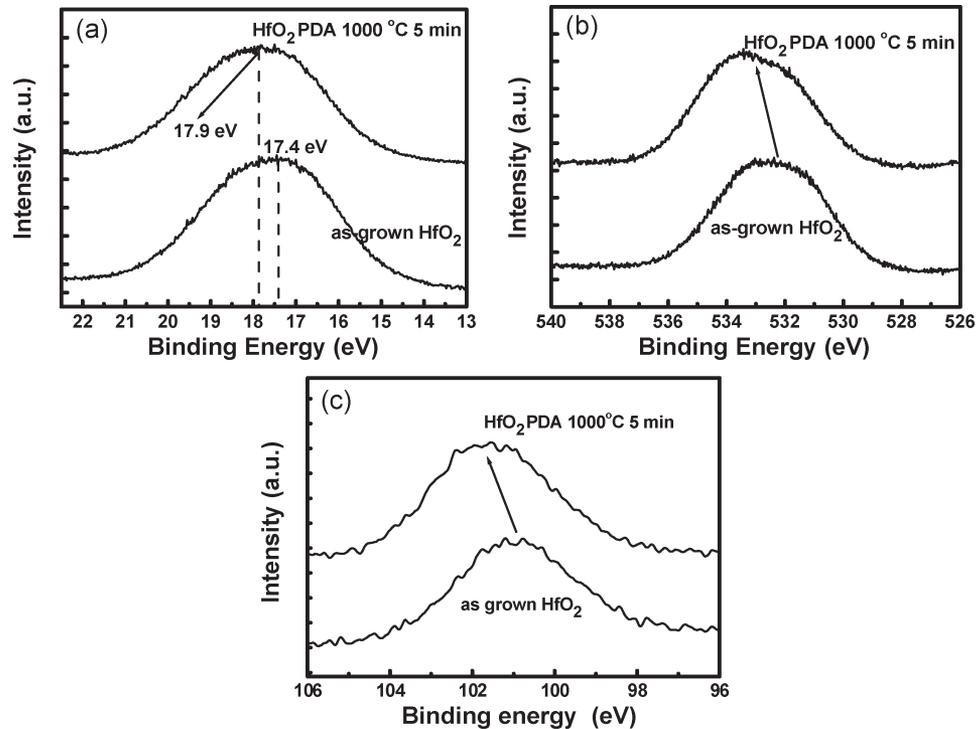


Fig. 4. XPS results of the (a) Hf 4f, (b) O 1s, and (c) Si 2p peaks for HfO₂ films as-grown and annealed at 1000 °C for 5 min in N₂ ambient. The shift of the Hf 4f, O 1s, and Si 2p peaks to higher binding energy is due to the formation of a Hf-silicate layer.

III. RESULTS AND DISCUSSION

A. FTIR Results

FTIR analysis is a reliable and powerful method due to its convenient, rapid, and nondestructive measurement. However, the high-*k* gate dielectrics are too thin to detect in FTIR measurement, and the characteristics of a thin HfO₂ layer may be quite different from those of a thick one due to the crystallization [13]. Previous work has shown that it is possible to detect the chemical bonding vibrations of SiO₂, HfO₂, and hydrogen defects in layers as thin as several nanometers using attenuated total reflection and multiple internal reflection measurements [14]–[16]. Those techniques use a complicated method to adjust the incident angle of the IR beam to enhance the signal. A simple method to detect the weak signal from the ultrathin film and from the interface between the high-*k* layer and the oxide is reported here. A schematic cross section of our “face-to-face” measurement structure is shown in Fig. 1(a). The absorption is enhanced in the “face-to-face” structure due to the doubling of the film thickness and the reflection at the interface between the surfaces of two samples. Note that there is no observable absorption signal if we use only one sample in our measurement system. Fig. 1(b) shows the FTIR spectra of HfO₂ after annealing at 600 °C for 1 min by the “face-to-face” measurement structure. Fig. 2 shows the FTIR spectra of the as-deposited HfO₂ sample and the HfO₂ samples annealed at 600 °C (1000 °C) in N₂ ambient for 1 min (5 min). There are three peaks, at 750, 600, and 512 cm⁻¹, attributed to the Hf–O chemical bonds in the spectra, and the peaks at 960, 900, and 820 cm⁻¹ are assigned to Hf–O–Si stretching vibrations [17]–[19]. The peak at around 1120 cm⁻¹ may result

from the absorption by the Si–N bond [20] formed by surface nitridation or from Si–O–Si bond absorption in the chemical oxide layer. The as-deposited HfO₂ film spectrum exhibits peaks at 600 and 512 cm⁻¹ due to Hf–O chemical bonds, and these two peaks increase with annealing temperature, so does the 750 cm⁻¹ peak. The intensity of the 750 cm⁻¹ peak increases with the thermal budget of the HfO₂ due to the increasing degree of crystallization [21]. Comparing the three spectra (as-deposited, annealed at 600 °C, and annealed at 1000 °C), the intensities of the peaks at 960, 900, and 820 cm⁻¹ are seen to increase with the thermal budget. This is due to the Hf–O–Si chemical bonds formed at the HfO₂/SiO₂ interface. The formation of silicate cannot be avoided during the thermal process. During the annealing process, Hf-silicate is formed by the atomic diffusion mechanism [22]. As a result of Si diffusion into HfO₂, the Hf-silicate should be formed in the original HfO₂ layer, thus increasing the interfacial layer thickness. The silicate interfacial layer shows no obvious contrast with the initial chemical oxide in the transmission electron microscopy (TEM) image (Fig. 5), indicating that the interfacial silicate might be Si rich. In Fig. 3, the FTIR absorption spectra of Hf-silicate are shown as a function of Hf concentration at different thermal annealing conditions. In Fig. 3(a), the as-deposited samples show no apparent peaks from 700 to 1000 cm⁻¹, which indicates that the as-deposited nanomixed HfO₂/SiO₂ does not intermix, whereas the peaks at 600 and 512 cm⁻¹ are detected in as-deposited nanomixed HfO₂/SiO₂, indicating Hf–O bond formation. The annealing at 600 °C and 1000 °C generates absorption peaks due to Hf–O–Si stretching vibrations [Fig. 3(b) and (c)], indicating the intermixture and formation of Hf-silicate after the thermal annealing of the nanomixed

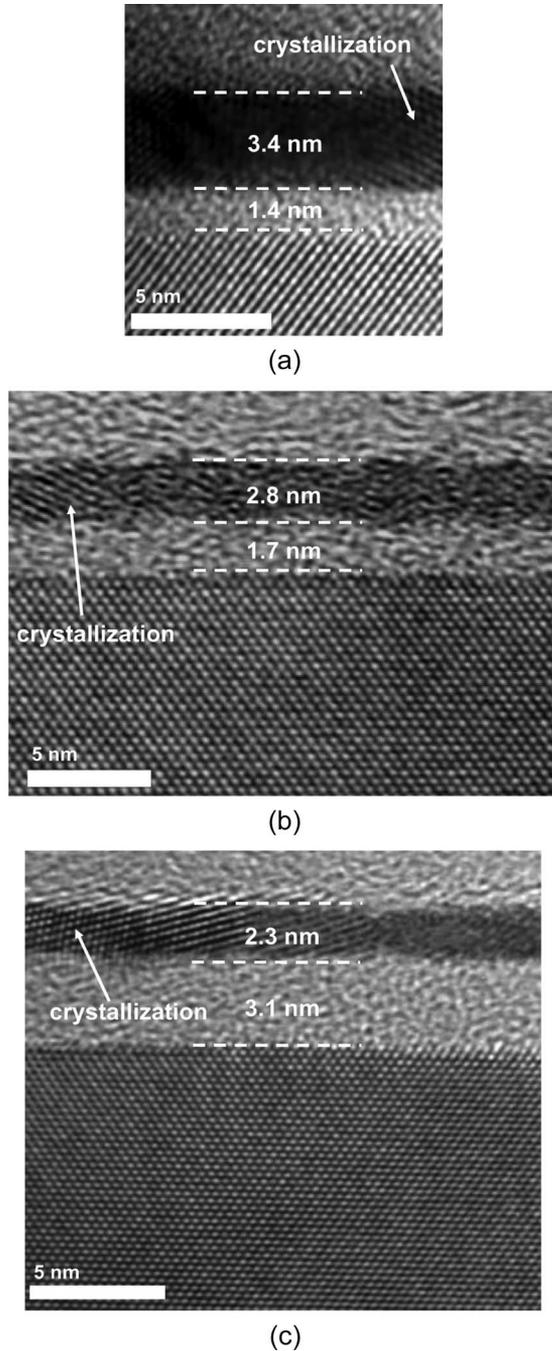


Fig. 5. HRTEM cross-sectional images of HfO₂ (a) as-deposited, (b) after 600 °C/1 min/N₂ annealing, and (c) after 1000 °C/5 min/N₂ annealing. The thickness of the interfacial layer between HfO₂ and Si is increased by annealing.

HfO₂/SiO₂. Moreover, the intensity of the peak at 750 cm⁻¹ also increases the Hf concentration in Fig. 3(b) and (c), indicating slight occurrence of phase separation and crystallization at the high Hf concentration [23]. Due to the variation in each measurement, in Fig. 3, the intensity of oxide has been normalized. The integration of the Hf–O–Si peak area is only studied as a function of temperature in the Hf_{0.66}Si_{0.34}O₂ samples. The ratio of this sample with PDA at 600 °C for 1 min and PDA at 1000 °C for 5 min is 2, which indicates the increase of Hf–O–Si bonds with the increase of temperature. Note that,

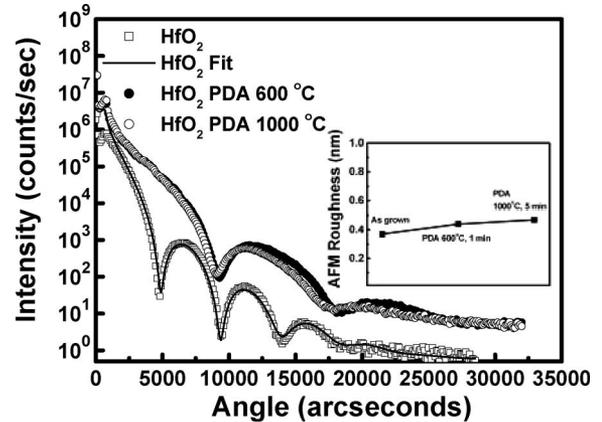


Fig. 6. XRR spectra of HfO₂ film as-deposited, after 1-min annealing in N₂ at 600 °C, and after 5-min annealing in N₂ at 1000 °C. The inset is the surface roughness for HfO₂ samples by AFM. The roughness of the HfO₂ surface is lower than 0.5 nm even after high-temperature annealing.

although the HfO₂ sample seems to have more pronounced Hf–O–Si peak (Fig. 2) than the nanomixed HfO₂/SiO₂ samples [Fig. 3(c)], the integration of the Hf–O–Si peak area (filtering the background of the broad signal) is slightly lower than the nanomixed HfO₂/SiO₂ samples. The quantitative analysis of the FTIR signal is not obtained by the difference of the optical alignment and environmental condition for each measurement.

B. Structural Analysis

The HfO₂ film on SiO₂ following PDA at 1000 °C is also characterized by XPS measurements, as shown in Fig. 4. In Fig. 4(a), the peak of the XPS of HfO₂ after the PDA process is at about 17.9 eV. The shift of Hf peak to higher binding energy is attributed to the formation of Hf-silicate film, which is 0.5 eV higher than that of the as-grown HfO₂ film (17.4 eV). Note that the two peaks (Hf 4f_{7/2} and Hf 4f_{5/2}) of XPS in Hf element cannot be distinguished due to the limit of resolution of the XPS equipment. Furthermore, the O 1s spectrum [Fig. 4(b)] and Si 2p spectrum [Fig. 4(c)] of the annealed samples are also shifted to higher energy as compared to those of as-grown sample, indicating a strong signature of Hf-silicate formation. The intensity of reflected X-ray is dependent on the surface and near-surface electron density. Consequently, XRR has been used to study a variety of surface and thin film properties. In order to understand the effects of the PDA process, it is important to realize the effect on the chemical reaction at interface and the growth of the interfacial layer due to the increasing thermal budget. XRR and TEM have been utilized to analyze the physical characteristics of as-grown and postannealed HfO₂. The TEM images of HfO₂ thin films as-deposited, annealed at 600 °C for 1 min, and annealed at 1000 °C for 5 min are shown in Fig. 5(a), (b), and (c), respectively. Fig. 5(a) shows that the as-grown HfO₂ partially crystallizes only after 600 °C for 10 s for the Si₃N₄ cap. The degree of crystallization of HfO₂ is clearly increased when the sample was annealed at 1000 °C, which is also indicated by the peak at 750 cm⁻¹ in Fig. 2. The thickness of the interfacial layer between the HfO₂ and the Si substrate increases with the thermal budget. Evidently, the HfO₂ film is partially consumed by the interfacial

TABLE I
DETAILS OF THE XRR MODEL SIMULATED DATA AS COMPARED TO TEM AND AFM RESULTS

As grown			PDA 600°C 1min			PDA 1000°C 5 min	
Simulation (model 1)		TEM	Simulation (model 2)		TEM	Simulation (model 2)	TEM
SiO ₂ (T)	1.3 nm	1.4 nm	SiO ₂ (T)	1.7 nm	1.7 nm	3.5 nm	3.6 nm
HfO ₂ (T)	3.4 nm	3.4 nm	Hf-silicate (T)	0.7 nm	2.8 nm	0.8 nm	2.3 nm
			HfO ₂ (T)	2 nm		1.8 nm	
Density (D)	HfO ₂ : 8.2 g/cm ³		Hf-silicate :	3 g/cm ³		3.2 g/cm ³	
			HfO ₂ :	8.4 g/cm ³		8.4 g/cm ³	
Surface Roughness (R)	0.41 nm (XRR) 0.37 nm (AFM)		0.42 nm (XRR) 0.44 nm (AFM)			0.46 nm (XRR) 0.47 nm (AFM)	

silicate, since the HfO₂ thickness decreases after annealing. Note that the Si₃N₄ cap is not visible in the TEM picture, which may be due to the poor contrast between Si₃N₄ and the TEM bonding glue or too thin thickness of the Si₃N₄ cap. Fig. 6 depicts the XRR response of the HfO₂ film before and after thermal annealing. The period of oscillation in the XRR plot depends on the thickness of the layer with the highest electron density, which is HfO₂ in our samples. The HfO₂ sample response has a longer period after N₂ annealing, indicating the shrinkage of the HfO₂ layers. This is confirmed by the TEM image in Fig. 5. Note that the XRR curves of annealed HfO₂ are obviously different from the as-deposited one due to the formation of the Hf-silicate layer and the increase of the interface roughness between each layer, which is obtained by XRR simulation. The HfO₂/SiO₂ interfacial roughness after annealing at 1000 °C for 5 min, which was obtained from the XRR fitting (not shown here), showed an increase of 0.3–0.4 nm as compared to the as-deposited sample. The inset of Fig. 6 is the surface roughness of the HfO₂, which is measured by AFM. The surface roughness of samples increases slightly after PDA [24]. The XRR of the annealed HfO₂ is fitted by the four-layer model (model 2 in Table I) that includes (in sequence): substrate Si, initial chemical oxide layer, Hf-silicate interfacial layer, and HfO₂ [25], and the as-deposited sample is also fitted with the structure without a Hf-silicate layer (model 1 in Table I). Note that although the layer of Hf-silicate is not clearly observed in the TEM picture, a Hf-silicate layer should be reasonably assumed between HfO₂ and SiO₂. Table I lists the corresponding layer thickness *T*, film density *D*, and surface roughness *R*, obtained by XRR fitting, TEM, and AFM. The surface roughness calculated by simulation is quite similar to that obtained from AFM measurement. The interface roughness in TEM is observed in extremely local region on the scale of

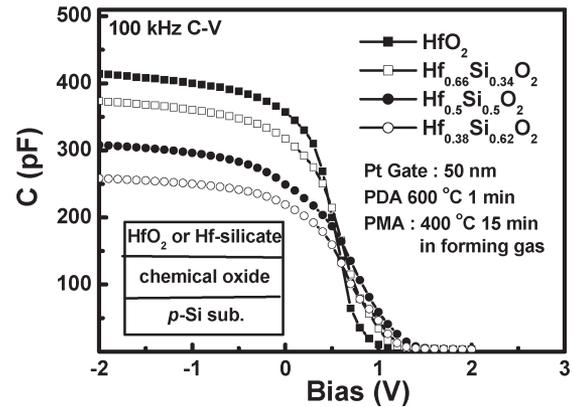


Fig. 7. High-frequency (100 kHz) *C–V* characteristics of Pt/HfO₂/Si and Pt/Hf-silicate/Si MIS capacitors following PDA at 600 °C for 1 min and PMA at 400 °C for 15 min. The inset shows the device structure. The *C–V* curve without PMA would be stretched out due to a large number of interface states (not shown).

nanometers. However, the roughness measured by XRR is an average on a large area (in square millimeters). The correlation between TEM roughness and XRR roughness is not known. The error bar of TEM thickness can be roughly estimated from each TEM micrograph. The thickness of the interfacial layer between the Si and the HfO₂ increases significantly when the thin film was annealed at 1000 °C for 5 min (Table I). The oxygen needed for the growth of the interfacial oxide may have originated from the trace oxygen in the flowing N₂ ambient and/or the oxygen in HfO₂ [26].

C. Electrical Properties

The high-frequency (100 kHz) *C–V* curves measured at room temperature are plotted in Fig. 7; the electrode area

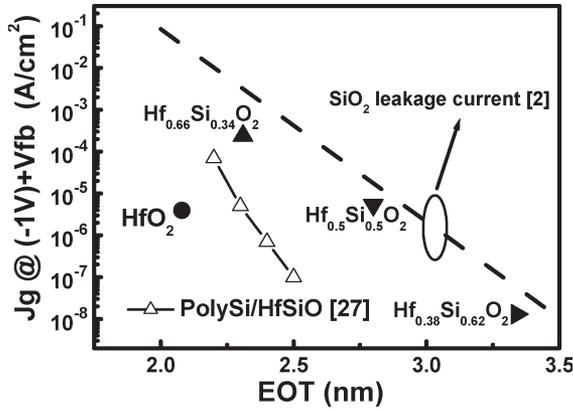


Fig. 8. Relation between leakage current and EOT of the HfO₂ and Hf-silicate devices. The SiO₂ trend line is also shown (dashed line).

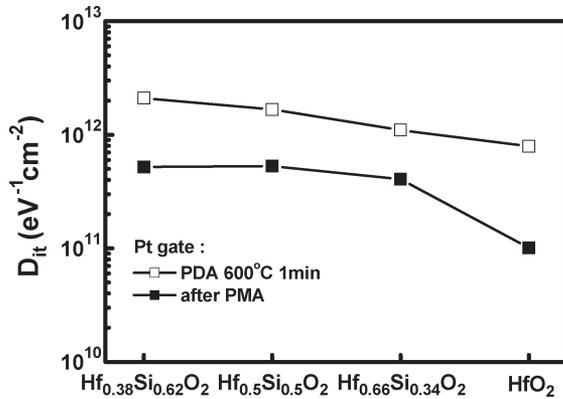


Fig. 9. D_{it} of HfO₂ and Hf-silicate devices with Pt electrodes before and after PMA. The D_{it} value of all high- k samples with PMA is below 10^{12} eV⁻¹ · cm⁻².

is 2.5×10^{-4} cm². The interfacial chemical oxide is grown by SC-1 before the insulator to restrict the interface trap density. The capacitance of the accumulation region increases, indicating an increase in the dielectric constant with increasing Hf concentration (38% to 100%). The gate leakage current density versus EOT of Hf_{*x*}Si_{1-*x*}O₂ at a gate voltage of flatband voltage (V_{FB}) - 1 for an n-channel metal-oxide-semiconductor diode is shown in Fig. 8. The HfO₂ device shows a $10^4 \times$ leakage current reduction as compared to the SiO₂ device, and the Hf-silicate devices show about one order of magnitude or less leakage current reduction as compared to the SiO₂ device. Note that the relatively larger leakage current as compared to [27] may be due to the larger interfacial layer thickness of our silicate samples. The thinner interfacial layer thickness (1 nm in [27]) with the sample EOT (larger physical thickness of the high- k layer) will thus reduce the leakage current [27], [28]. The interface trap density D_{it} at V_{FB} measured by the high-low (100 and 1 kHz) frequency method is shown in Fig. 9. The D_{it} of these devices is of the order of 1×10^{12} cm⁻² · eV⁻¹ without PMA and is reduced by about one order of magnitude after a PMA process. The HfO₂ device has the smallest D_{it} both after PMA and before PMA, indicating that the Si in Hf-silicate can create interface defects at the HfO₂/SiO₂ interface.

IV. CONCLUSION

In summary, we find that the weak FTIR signal from thin Hf_{1-*x*}Si_{*x*}O₂ films can be detected by using a “face-to-face” structure. When the HfO₂ thin film on oxide is annealed at high temperature (600 °C and 1000 °C), the formation of a Hf-silicate layer in the HfO₂/SiO₂ interface is evidenced by the occurrence of the FTIR peaks at 960, 900, and 820 cm⁻¹, attributed to Hf-Si-O stretching vibrations. Moreover, XPS results also indicate a great signature of the formation of a Hf-silicate layer. The annealed nanomixed HfO₂/SiO₂ sample has the same FTIR peaks, indicating that the nanomixed HfO₂/SiO₂ transformed into Hf-silicate after the thermal process.

ACKNOWLEDGMENT

The authors would like to thank Dr. M. Temple for proof-reading this paper.

REFERENCES

- [1] *International Technology Roadmap for Semiconductors*, Semiconductors Industry Association, San Jose, CA, 2001.
- [2] Y.-C. Yeo, T.-J. King, and C. Hu, “MOSFET gate leakage modeling and selection guide for alternative gate dielectrics based on leakage considerations,” *IEEE Trans. Electron Devices*, vol. 50, no. 4, pp. 1027–1035, Apr. 2003.
- [3] C. Hobbs, H. Tseng, K. Reid, B. Taylor, L. Dip, L. Hebert, R. Garcia, R. Hegde, J. Grant, D. Gilmer, A. Franke, V. Dhandapani, M. Azrak, L. Prabhu, R. Rai, S. Bagchi, J. Conner, S. Backer, F. Dumbuya, B. Nguyen, and P. Tobin, “80 nm poly-Si gate CMOS with HfO₂ gate dielectric,” in *IEDM Tech. Dig.*, 2001, pp. 651–654.
- [4] G. D. Wilk, R. M. Wallace, and J. M. Anthony, “High- k gate dielectrics: Current status and materials properties considerations,” *J. Appl. Phys.*, vol. 89, no. 10, pp. 5243–5275, 2001.
- [5] R. Choi, C. S. Kang, B. H. Lee, K. Onishi, R. Nieh, S. Gopalan, E. Dharmarajan, and J. C. Lee, “High-quality ultra-thin HfO₂ gate dielectric MOSFETs with TaN electrode and nitridation surface preparation,” in *VLSI Symp. Tech. Dig.*, 2001, pp. 15–16.
- [6] S. Pidin, Y. Morisaki, Y. Sugita, T. Aoyama, K. Irino, T. Nakamura, and T. Sugii, “Low standby power CMOS with HfO₂ gate oxide for 100-nm generation,” in *VLSI Symp. Tech. Dig.*, 2002, pp. 28–29.
- [7] R. Chau, S. Datta, M. Doczy, B. Doyle, J. Kavalieros, and M. Metz, “High- κ /metal-gate stack and its MOSFET characteristics,” *IEEE Electron Device Lett.*, vol. 25, no. 6, pp. 408–410, Jun. 2004.
- [8] P. Panchaipetch, G. Pant, M. A. Quevedo-Lopez, C. Yao, M. El-Bouanani, M. J. Kim, R. M. Wallace, and B. E. Gnade, “Low-temperature deposition of hafnium silicate gate dielectrics,” *IEEE J. Sel. Topics Quantum Electron.*, vol. 10, no. 1, pp. 89–100, Jan./Feb. 2004.
- [9] W. Zhu, T. P. Ma, T. Tamagawa, Y. Di, J. Kim, R. Carruthers, M. Gibson, and T. Furukawa, “HfO₂ and HfAlO for CMOS: Thermal stability and current transport,” in *IEDM Tech. Dig.*, 2001, pp. 463–466.
- [10] M.-Y. Ho, H. Gong, G. D. Wilk, B. W. Busch, M. L. Green, W. H. Lin, A. See, S. K. Lahiri, M. E. Loomans, P. I. Raisanen, and T. Gustafsson, “Suppressed crystallization of Hf-based gate dielectrics by controlled addition of Al₂O₃ using atomic layer deposition,” *Appl. Phys. Lett.*, vol. 81, no. 22, pp. 4218–4220, Nov. 2002.
- [11] E. Vainonen-Ahlgren, E. Tois, T. Ahlgren, L. Khriachtchev, J. Marles, S. Haukka, and M. Tuominen, “Atomic layer deposition of hafnium and zirconium silicate thin films,” *Comput. Mater. Sci.*, vol. 27, no. 1/2, pp. 65–69, 2003.
- [12] D.-S. Kil, K. Hong, K.-J. Lee, J. Kim, H.-S. Song, K.-S. Park, J.-S. Roh, H.-C. Sohn, J.-W. Kim, and S.-W. Park, “Development of highly robust nano-mixed Hf_{*x*}Al_{1-*x*}O₂ dielectrics for TiN/Hf_{*x*}Al_{1-*x*}O₂/TiN capacitor applicable to 65 nm generation DRAMs,” in *VLSI Symp. Tech. Dig.*, 2004, pp. 126–127.
- [13] T.-H. Moon, M.-H. Ham, M.-S. Kim, I. Yun, and J.-M. Myoung, “Growth and characterization of MOMBE grown HfO₂,” *Appl. Surf. Sci.*, vol. 240, no. 1–4, pp. 105–111, 2005.

- [14] V. Cosnier, M. Olivier, G. Theret, and B. Andre, "HfO₂-SiO₂ interface in PVD coatings," *J. Vac. Sci. Technol. A, Vac. Surf. Films*, vol. 19, no. 5, pp. 2267-2271, 2001.
- [15] C. W. Liu, Y. H. Huang, C. Y. Chen, S. Gurtler, C. C. Yang, Y. Chang, and L. P. Chen, "Infrared studies of laser induced oxide on (100) Si and SiGe layers," *Mater. Chem. Phys.*, vol. 65, no. 3, pp. 350-353, 2000.
- [16] N. Rochat, K. Dabertrand, V. Cosnier, S. Zoll, P. Besson, and U. Weber, "Infrared spectroscopy of high-*k* thin layer by multiple internal reflection and attenuated total reflection," *Phys. Stat. Sol. (C)*, vol. 0, no. 8, pp. 2961-2965, 2003.
- [17] D. A. Neumayer and E. Cartier, "Materials characterization of ZrO₂-SiO₂ and HfO₂-SiO₂ binary oxides deposited by chemical solution deposition," *J. Appl. Phys.*, vol. 90, no. 4, pp. 1801-1808, 2001.
- [18] Q. Fang, J.-Y. Zhang, Z. Wang, M. Modreanu, B. J. Oullican, P. K. Hurley, T. L. Leedham, D. Hywel, M. A. Audier, C. Jimenez, J.-P. Senateur, and I. W. Boyd, "Interface of ultrathin HfO₂ films deposited by UV-photo-CVD," *Thin Solid Films*, vol. 453/454, pp. 203-207, 2004.
- [19] X. Zhao and D. Vanderbilt, "First-principles study of structural, vibrational, and lattice dielectric properties of hafnium oxide," *Phys. Rev. B.*, vol. 65, no. 23, p. 233 106, 2002.
- [20] M. Ito, K. Kamiya, M. Hori, and T. Goto, "Subsurface reactions of silicon nitride in a highly selective etching process of silicon oxide over silicon nitride," *J. Appl. Phys.*, vol. 91, no. 5, pp. 3452-3458, 2002.
- [21] D. Triyoso, R. Liu, D. Roan, M. Ramon, N. V. Edwards, R. Gregory, D. Werho, J. Kulik, G. Tam, E. Irwin, X.-D. Wang, L. B. La, C. Hobbs, R. Garcia, J. Baker, B. E. White, Jr., and P. Tobin, "Impact of deposition and annealing temperature on material and electrical characteristics of ALD HfO₂," *J. Electrochem. Soc.*, vol. 151, no. 10, pp. F220-F227, 2004.
- [22] C. C. Yeo, M. S. Joo, B. J. Cho, and S. J. Whang, "Effect of annealing on the composition and structure of HfO₂ and nitrogen-incorporated HfO₂," *Thin Solid Films*, vol. 462/463, pp. 90-95, 2004.
- [23] M.-H. Cho, K. B. Chung, C. N. Whang, D. W. Lee, and D.-H. Ko, "Phase separation and electronic structure of Hf-silicate film as a function of composition," *Appl. Phys. Lett.*, vol. 87, no. 24, p. 242 906, 2005.
- [24] R. Ludeke and E. Gusev, "Structural and surface potential characterization of annealed HfO₂ and (HfO₂)_x(SiO₂)_{1-x} films," *J. Appl. Phys.*, vol. 96, no. 4, pp. 2365-2373, 2004.
- [25] P. S. Lysaght, P. J. Chen, R. Bergmann, T. Messina, R. W. Mutro, and H. R. Huff, "Experimental observations of the thermal stability of high-*k* gate dielectric materials on silicon," *J. Non-Cryst. Solids*, vol. 303, no. 1, pp. 54-63, May 2002.
- [26] P.-J. Tzeng, S. Maikap, P.-S. Chen, Y.-W. Chou, C.-S. Liang, and L.-S. Lee, "Physical and reliability characteristics of Hf-based gate dielectrics on strained-Si_{1-x}Ge_x MOS devices," *IEEE Trans. Device Mater. Rel.*, vol. 5, no. 2, pp. 168-176, Jun. 2005.
- [27] A. Morioka, H. Watanabe, M. Miyamura, T. Tatsumi, M. Saitoh, T. Ogura, T. Iwamoto, T. Ikarashi, Y. Saito, Y. Okada, H. Watanabe, Y. Mochiduki, and T. Mogami, "High mobility MISFET with low trapped charge in HfSiO films," in *VLSI Symp. Tech. Dig.*, 2003, pp. 165-166.
- [28] Y. Y. Fan, Q. Xiang, J. An, L. F. Register, and S. K. Banerjee, "Impact of interfacial layer and transition region on gate current performance for high-*k* gate dielectric stack: Its tradeoff with gate capacitance," *IEEE Trans. Electron Devices*, vol. 50, no. 2, pp. 433-439, Feb. 2003.



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