

# Electroluminescence at Si Bandgap Energy from Metal-Oxide-Semiconductor Tunneling Diodes

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## Abstract

We report room-temperature electroluminescence at Si bandgap energy from Metal-Oxide-Semiconductor (MOS) tunneling diodes. The ultrathin gate oxide with thickness 1 ~ 3 nm was grown by rapid thermal oxidation (RTO) to allow significant current to tunnel through. The measured EL efficiency of the MOS tunneling diodes increases with the injection current and could be in the order of  $10^{-5}$ , which exceeds the limitation imposed by indirect bandgap nature of Si. We also study the temperature dependence of the electroluminescence and photoluminescence. The electroluminescence is much less dependent on temperature than photoluminescence from Si. The applied external field that results in the accumulation of majority carriers at Si/SiO<sub>2</sub> interface in the case of electroluminescence could be the reason for such difference. The involved physics such as optical phonon, interface roughness, localized carriers, and exciton radiative recombination are used to explain the electroluminescence from silicon MOS tunneling diodes.

**Keywords** — Silicon, MOS, electroluminescence.

## 1. Introduction

Silicon is the most important semiconductor material for electronics industry, but has limited applications in area of optics and optoelectronics due to its indirect bandgap characteristics that make it hardly emit light. In order to convert silicon to a light-emitting material, many efforts had been tried before, including porous silicon, nanocrystalline Si, Si/Ge alloy, and Erbium doping of Si, and so on<sup>1</sup>. However, this luminescence originates from the defect-related mechanisms and the fabrication processes are complicated. In this paper, we report efficient electroluminescence (EL) at Si bandgap from metal/SiO<sub>2</sub>/Si Metal-Oxide-Semiconductor tunneling diodes<sup>2,3</sup>. The characteristics of the light-emitting MOS tunneling diodes on silicon are

1. The structure of MOS tunneling diode is the same as that used in Ultra-Large-Scale-Integration (ULSI) circuits,
2. Using tunneling current through the ultrathin oxide (15 ~ 35 Å) to provide carriers for radiative recombination,
3. The emission wavelength is near silicon bandgap, and
4. The fabrication processes are fully compatible with the present ULSI technology.

As a result, the light-emitting MOS tunneling diodes show promise to achieve the practical on-chip light source for optical interconnection and silicon-based optoelectronics integrated circuits (OEIC).

The temperature dependence of electro- and photoluminescence (PL) was also investigated to study the luminescent properties of the silicon MOS tunneling diodes. The EL was found to be less dependent on temperature than PL from Si. The reason is attributed to the accumulation of majority carriers at Si/SiO<sub>2</sub> interface due to the applied external field in the EL case. A physical process involving phonon assistance and exciton radiative recombination is used to fit the measured electroluminescence spectra. The excellent match of the calculated spectrum to the measured data of EL spectra shows that the exciton radiative recombination and the participation of optical phonon take place in the room-temperature EL from the silicon MOS tunneling diodes.

## 2. Device Fabrication

When the devices are scaled down in the state-of-the-art ULSI chips, ultra-thin gate oxides are needed to reduce short channel effects. However, current tunneling through the ultra-thin gate oxide inevitably increases when the thickness of the gate oxide shrinks with each generation of IC fabrication technology. Therefore, the devices based on the tunneling current are fabricated and developed. In order to grow the high quality ultra-thin gate oxide, the rapid thermal oxidation (RTO) is commonly used to meet this requirement<sup>4</sup>.

In this work, the MOS tunneling diode has the ultrathin gate oxide to make significant current tunnel through. The ultrathin gate oxide was grown by rapid thermal oxidation (RTO) on n-type (100) Si wafer at 900 -1000°C. The resistivity of Si substrate are 1~10  $\Omega$ -cm. The gas flows are 500 sccm nitrogen and 500 sccm oxygen at a reduced pressure. The thickness of oxide is measured by ellipsometry and confirmed by high-frequency capacitance-voltage measurements. The oxide thickness is in the range of 15 ~ 35 Å. Aluminum or transparent indium tin oxide (ITO) was deposited as the gate electrode using the usual evaporator. The gate electrodes with various circular areas are defined by photolithography. The thickness of Si substrate is about 500  $\mu$ m and aluminum was deposited on the backside as another electrode of the MOS diode. The device structures are schematically shown in Fig.1.

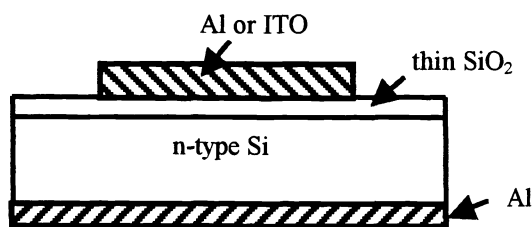


Fig.1 schematic diagram of the MOS tunneling diode

## 2. I-V Characteristics, Electroluminescence Spectra and Quantum Efficiency

Fig. 2 shows the typical I-V characteristics of the MOS tunneling diodes on n-type silicon and Fig.3 is the schematic band diagram. The I-V curve shows the rectifying characteristics. When positive voltage is applied at the metal gate, the diode operates in the accumulation region. The positive gate voltage will inject holes from metal to silicon and attract electrons at the Si/SiO<sub>2</sub> interface to form an accumulation layer. The tunneling holes then recombine with the localized electrons in the accumulation layer to generate radiative emission, as schematically shown in Fig.3. The electrons accumulating at the Si/SiO<sub>2</sub>

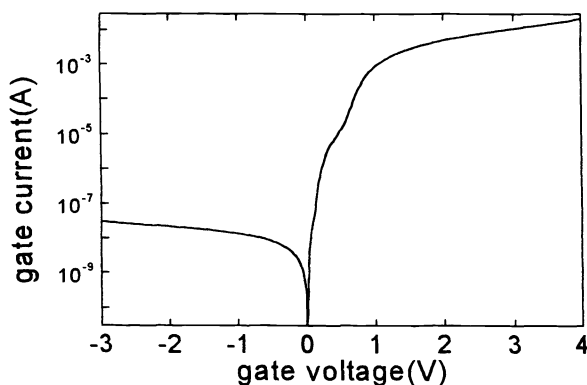


Fig.2 I-V characteristics of the tunneling diode on n-type Si substrate

interface also tunnel through the thin oxide from the silicon to metal. The electron tunneling current is much larger than the hole tunneling current due to the larger tunneling barrier for hole. Even though the electron tunneling current acts as the leakage current for radiative recombination, EL was still observed. Note that there is a kink at about 0.8 V, which implies two different current tunneling mechanisms<sup>5</sup>. At small positive gate voltage, the current was primarily contributed the electrons in conduction band tunneling from the silicon to metal gate through interface states. As the voltage increases above 0.8 V, the holes tunneling from the metal to silicon contribute to the current. When the gate is biased at negative voltage, an inversion layer of the holes is formed at Si/SiO<sub>2</sub> interface. As long as the holes in the inversion layer are generated, they tunnel away to the metal gate and the inversion layer is always kept at the onset of the inversion region, instead of the stronger inversion. The reverse-biased current is limited by the generation rate of the holes through the Si/SiO<sub>2</sub> interface state and so is much smaller than the forward biased current.

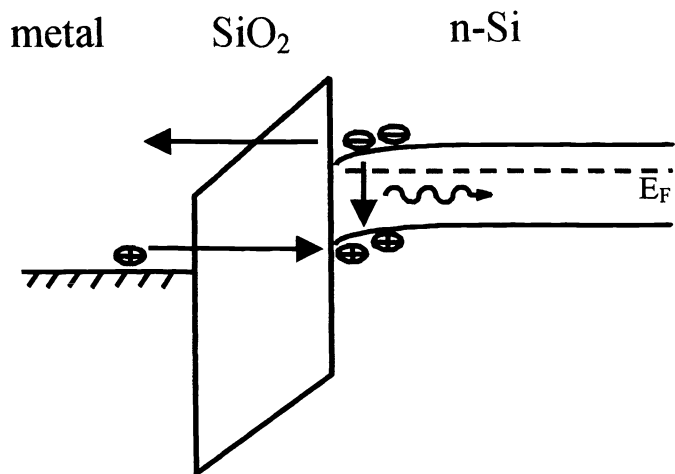


Fig.3 Schematic band diagram of MOS tunneling diode operated in the accumulation region.

When significant current tunnels through the thin oxide, EL is clearly observed through an IR-viewer even with a very low injection current density,  $< 1 \text{ A/cm}^2$ . The diode is operated at room temperature under continuous wave operation without cooling. Since the luminescence was almost blocked by the thick Al gate electrode, only the emission from the gate electrode edge can be measured. Fig.4 shows the emission spectra at various injection current of Al/30Å oxide/n-Si with a gate electrode area of  $8 \times 10^{-3} \text{ cm}^2$ . These spectra were measured using an InGaAs detector placed at the exit of the monochromator. The emission wavelength is near the silicon bandgap energy.

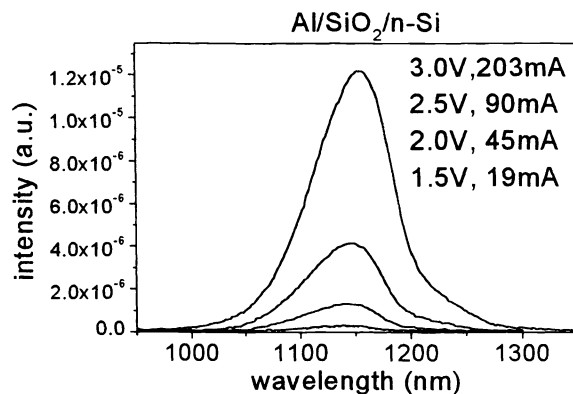


Fig.4 The electroluminescence spectra from Al/30Å oxide/n-Si MOS tunneling diode

Because the EL is almost blocked by the metal gate, we measure the optical power from the backside of the MOS diodes to estimate the quantum efficiency. Before the measurement, the Al metal gate at the back Si side was first lapped off. The backside Si surface was then polished mechanically and chemically. Afterward Al was redeposited, but not directly under the MOS area, as shown in Fig. 5. Hence the emitted power was not blocked by Al metal. Fig. 6 shows the measured spectrum from the back Si side and from the metal gate side. It demonstrates that the EL from the back Si side is narrower than that measured from the metal gate side because the short-wavelength part is absorbed by the Si substrate, which is about 500  $\mu\text{m}$  thick. Therefore, the measured optical power is attenuated by the absorption of the Si substrate. The measured L-I characteristics from the back Si side of the Al/30 $\text{\AA}$  oxide/n-Si MOS tunneling diode were shown in Fig. 7. A superlinear increase of the output power with the injection current is shown. The efficiency increases with the injection current and the measured slope efficiency is  $8 \times 10^{-6}$  at injection current 70 mA. Note that the injection current density ( $\sim 4 \text{ A/cm}^2$ ) in this measurement is still

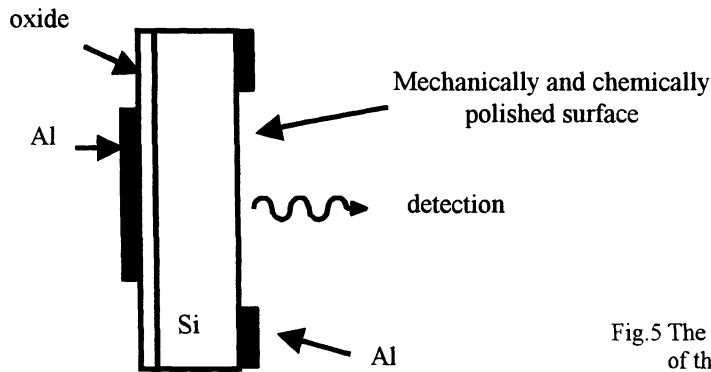


Fig.5 The schematic diagram for measurement of the optical power

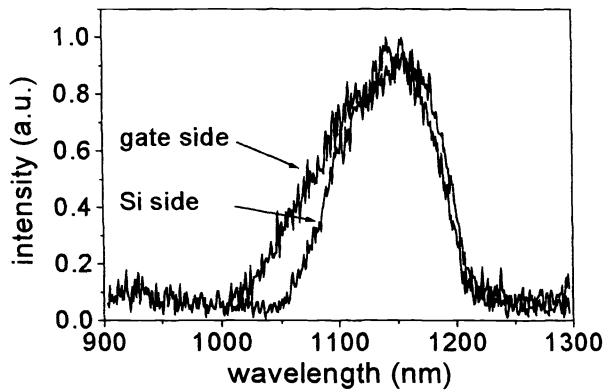


Fig.6 The measured spectra from metal gate side and back Si side

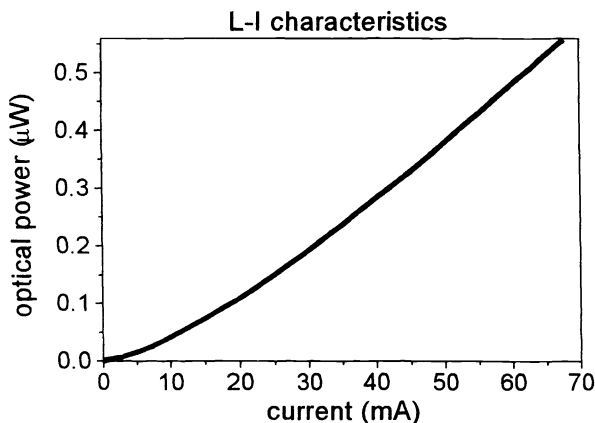


Fig.7 The measured L-I curves from the back Si side of the Al/30 $\text{\AA}$  oxide/n-Si MOS tunneling diodes

very low. The typical slope efficiency is estimated to be in the order of  $10^{-5}$  for the room-temperature EL from MOS tunneling diodes on silicon. Better EL efficiency is expected with improved process condition and device structures, such as the current confinement and waveguide structures.

### 3. Temperature Effects on Electroluminescence and Photoluminescence

To further study the luminescent properties of the MOS tunneling diodes, the temperature dependence of electro- and photoluminescence was investigated<sup>6,7</sup>. The EL spectra of the Al/30Å oxide/n-Si MOS diode from 300K down to 98K are shown in Fig.8. The gate voltage and injection current density are fixed at 3V and  $\sim 0.4 \text{ A/cm}^2$ . Fig.9 shows the PL spectra from Si crystal under Argon laser excitation at different temperatures. Careful comparison between Fig.8 and Fig.9 reveals that the EL spectra are similar to PL ones. It is well known that the transverse optical (TO) phonon with energy 57.8 meV is required for momentum conservation in PL.<sup>8</sup> The resemblance between the electro- and photoluminescence spectra confirms the TO phonon participation of the TO phonon in the EL from MOS tunneling diodes on silicon.

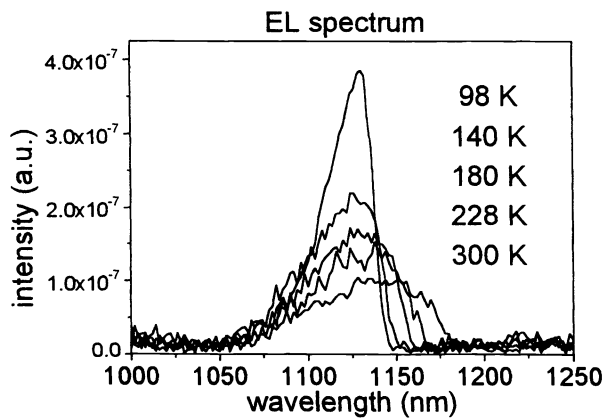


Fig.8 The electroluminescence spectra from Al/30Å oxide/n-Si MOS tunneling diode at various temperature

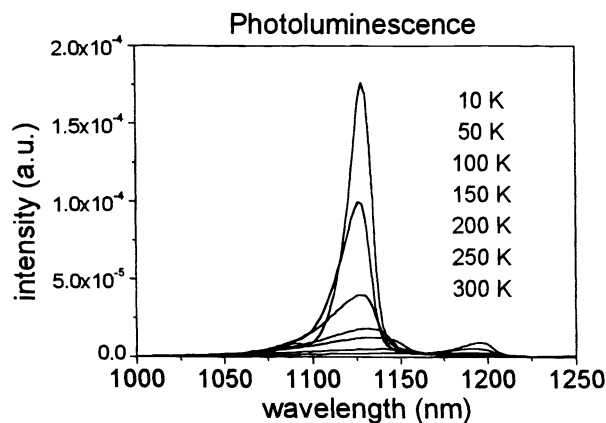


Fig.9 The photoluminescence spectra from Si crystal at various temperature

The EL was also discovered to be less dependent on temperature than the PL from Si. Fig.10 shows the temperature dependence of electro- and photoluminescence intensity. The experiment demonstrates that the EL intensity of MOS

tunneling diodes exhibits weak temperature dependence, as compared to PL. The reason is attributed to carrier confinement in the accumulation layer, which is related to the gate voltage and is almost independent of the temperature. On the other hand, the carrier confined in a small region is also captured by less impurity states, and therefore results in a reduced temperature dependence.

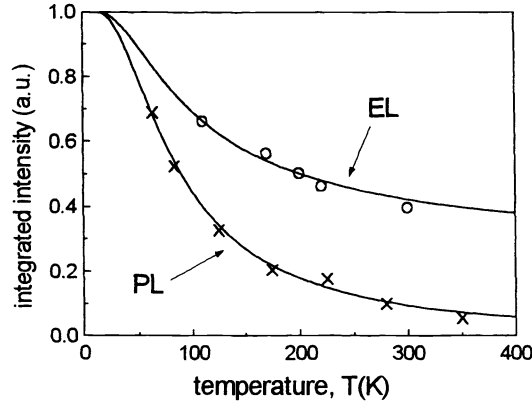


Fig.10 Temperature dependence of electroluminescence and photoluminescence (o, x: experimental results; solid line: theoretical prediction.)

#### 4. Radiative Recombination through Exciton

We develop a physical model to describe the EL from the MOS tunneling on silicon. In order to fit the measured EL spectra, we found that the effect of exciton needs to be taken into account. Before recombination, an electron and a hole are first bounded together under Coulomb attraction to form an exciton, and then recombine to emit a photon and a phonon.<sup>9</sup> We derived the spectral shape of luminescence from Si in terms of the phonon-assisted and exciton radiative recombination as follows :

$$\begin{aligned}
 S(\hbar\omega) \sim & \sum_n \frac{1}{(2\pi)^3} \iint_{Kq} d^3K d^3q \cdot |\phi_n(0)|^2 L(E_g - \frac{E_B}{n^2} + \frac{\hbar^2 K^2}{2M} - E_p - \hbar\omega) \delta(\bar{q} - \bar{k}_e - \bar{k}_h) \cdot \frac{1}{\exp(\frac{E_g - \frac{E_B}{n^2} + \frac{\hbar^2 K^2}{2M} - F_x}{k_B T}) - 1} \\
 & + \frac{1}{(2\pi)^3} \frac{1}{(2\pi)^3} \iiint_{kKq} d^3k d^3K d^3q \cdot |\phi(0)|^2 L(E_g + \frac{\hbar^2 k^2}{2\mu} + \frac{\hbar^2 K^2}{2M} - E_p - \hbar\omega) \delta(\bar{q} - \bar{k}_e + \bar{k}_h) \cdot \frac{1}{\exp(\frac{E_g + \frac{\hbar^2 k^2}{2\mu} + \frac{\hbar^2 K^2}{2M} - F_x}{k_B T}) - 1}
 \end{aligned} \quad (1)$$

where  $n$  is the quantum number of discrete exciton states;  $K$  and  $k$  are the center-of-mass and the relative wavevector of exciton;  $q$  is the wavevector of phonon;  $k_e$  and  $k_h$  are the wavevectors of the electron and hole composing the exciton, respectively;  $M$  and  $\mu$  are the total mass and the reduced mass of the electron-hole pair;  $E_g$  is the temperature-dependent bandgap energy of Si;  $E_B$  is the exciton binding energy;  $E_p$  is the energy of phonon participating the radiative transition;  $F_x$  is the quasi-chemical potential of the exciton;  $k_B$  is the Boltzmann constant and  $T$  is the lattice temperature. The first term in the expression is the contribution from discrete exciton states. This term sums over all quantum numbers  $n$ . The second term corresponds to the ionized continuum exciton states.  $|\phi_n(0)|^2$  and  $|\phi(0)|^2$  are the probability of finding the electron and hole at the same space

point. Because of the requirement of symmetric wavefunction in the spatial coordinate ( $|\phi_n(0)|^2$  and  $|\phi(0)|^2 \neq 0$ ), only the singlet spin state is taken into account in the expression. The Lorentzian linewidth broadening function  $L$  is used to account for the effect of carrier scattering. The delta function in the integral represents the conservation of momentum. Because the phonon used to compensate the mismatch of momentum lies near the Brillouin zone edge, it is a good approximation to assume that the phonon energy  $E_p$  is independent of its wavevector  $q$ . The temperature dependence of bandgap energy  $E_g$  is given in Ref. 10. The widely accepted value of the exciton binding energy  $E_B = 14.7$  meV is used for the calculation.<sup>9</sup> The emission of TO phonon with  $E_p = 57.8$  meV is known as the dominate mechanism for momentum conservation for radiative recombination.<sup>8</sup> In the derivation of (1), we use the Bose-Einstein statistics obeyed by exciton. Assuming that the quasi-chemical potential  $F_x$  of exciton is below the indirect bandgap over many  $kT$ , the Bose-Einstein function is reduced to the Boltzmann distribution. There are only two fitting parameters, the lattice temperature  $T$  and the intra-band carrier scattering time  $\tau_s$  in the Lorentzian function  $L$ , necessary to fit our theoretical lineshape (1) and the measured spectra from the MOS silicon tunneling diodes.

The EL spectra of the ITO/30Å oxide/*n*-Si tunneling diode are shown in Fig. 11. The dashed lines are calculated spectra using (1). Because the measurement is performed at room temperature and the injection current density is low,  $T = 300\text{K}$ ,  $310\text{K}$  and  $\tau_s = 60$  fs in the Lorentzian broadening function are used. The value of  $\tau_s$  is in good agreement with the intraband carrier scattering time in Si. The long-wavelength tails in the Fig. 11 is due to the participation of two-phonon process in the radiative recombination. The PL spectrum of the same device at low temperature is shown in Fig. 12. There are three main peaks corresponding to two-phonon, TO, and TA phonon-assisted recombination processes.<sup>8</sup> The two-phonon process involves the emission of a momentum-conserving TO phonon and a zone center ( $O^\Gamma$ ) phonon.<sup>8</sup> When the temperature increases to room temperature, the signal of two-phonon process in the PL spectrum gradually mixes with the main TO peak

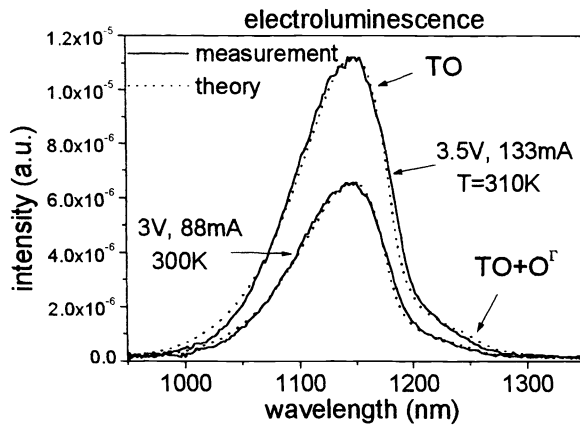


Fig. 11 Room-temperature electroluminescence spectra from ITO/30Å SiO<sub>2</sub>/*n*-Si tunneling diode as well as the theoretical fitting.

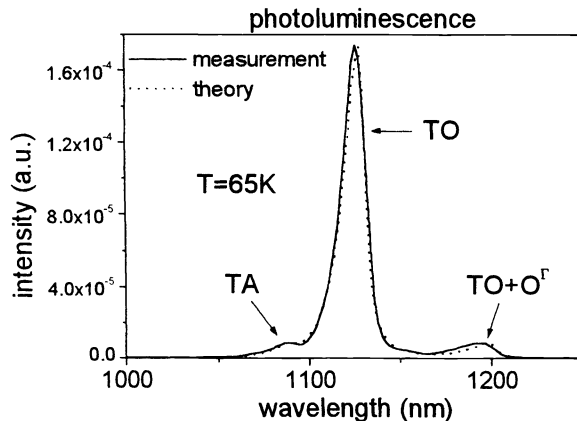


Fig. 12 Low-temperature photoluminescence spectrum from ITO/30Å SiO<sub>2</sub>/*n*-Si tunneling diode

and forms the low-energy tail similar to Fig.11. The emission spectrum of two-phonon process is obtain using (1) with  $E_p = 57.8+64.5 \text{ meV (TO+O}^\Gamma)$ .<sup>8</sup> The ratio of the magnitude between the two-phonon and TO peaks is 0.07, which is the same as the relative magnitude of the two signals in the PL spectrum at 26K.<sup>8</sup> The excellent fitting of the theoretical spectrum to the measured data in Fig.11 indicates that the exciton radiative recombination and the participation of optical phonon could take place in the room-temperature EL from the MOS silicon tunneling diodes.

## 5. Discussions and Summary

Possible reasons for the occurrence of EL from the MOS silicon tunneling diodes at room temperature are discussed as follows. Because of the indirect bandgap nature of Si, some mechanisms are required for momentum conservation in the radiative recombination. In addition to the participation of phonons, the carrier scattering by Si/SiO<sub>2</sub> interface roughness is shown to be important to provide some momentum to compensate the momentum-mismatch between the electrons and holes in the k-space.<sup>11</sup> Therefore, more phonons with momentum complementary to the interface roughness scattering are involved in the radiative recombination. The localized majority carriers in the accumulation region will also result in the spread of momentum in the k-space and provide a similar function to that of the interface roughness. The spreading of wavefunction in the k-space due to the interface roughness and localized carriers could enhance EL for Si.

It has been proposed that the effect of quantum confinement will assist the formation of excitons and enhance the probability of radiative transitions.<sup>12</sup> The dimension of the quantum confinement system is typically in the order of the Bohr radius of exciton. The roughness at Si/SiO<sub>2</sub> boundary could provide the weak two-dimensional quantum confinement along the plane of interface. The carriers are also confined in the accumulation layer due to the applied electrical field. The interface roughness and electrical field then result in a three-dimensional quantum confinement for excitons. Therefore, in addition to the extra momentum contributed by the carrier scattering due to the interface roughness, the roughness could improve the formation of exciton for enhanced luminescence.

In summary, room-temperature EL from MOS tunneling diode on silicon was reported. The high-quality ultra-thin SiO<sub>2</sub> layer was grown by the rapid thermal oxidation to allow significant tunneling current taking place. The emission wavelength corresponds to silicon bandgap energy and the slope efficiency could be in the order of 10<sup>-5</sup>. The temperature dependence of electro- and photoluminescence was also studied. The less temperature dependence of the electroluminescence is attributed to the carrier confinement in the accumulation layer. The resemblance between the electro- and photoluminescence spectra reveals the participation of the TO phonon in the EL. The EL for MOS silicon tunneling diodes is analyzed in terms of phonon-assisted and exciton radiative recombination. There is excellent match between the theoretical spectra and measured data. Some mechanisms, such as the carriers scattering by the Si/SiO<sub>2</sub> interface roughness, and the spread of momentum in k-space due to the localized majority carriers in the accumulation layer, are used to explain the high-efficient radiative recombination in MOS silicon tunneling diodes.

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