# Scheduling Algorithm for Nonpreemptive Multiprocessor Tasks 

J.-F. Lin and S.-J. Chen<br>Department of Electrical Engineering National Taiwan University Taipei, Taiwan 106, R.O.C.

(Received May 1993; accepted June 1993)


#### Abstract

This paper considers the problem of scheduling nonpreemptive multiprocessor tasks in a homogeneous system of processors. The problem proposed in this paper is different from the conventional scheduling problem, where each task requires only "one" processor whenever it is in processing. In our multiprocessor task scheduling problem, tasks can require more than one processor at a time for their processing, that is, tasks in our problem require simultaneously " $j$ " arbitrary processors when they start to be processed, where $1 \leq j \leq k$ and $k$ is a fixed number. Though multiprocessor tasks considered in this paper are assumed to be independent, i.e., the precedence relation does not exist among them, such a problem of scheduling nonpreemptive multiprocessor tasks is NP-complete. Therefore, a heuristic algorithm is investigated for this kind of problem and the worst performance bounds of the algorithm are also derived.


Keywords-Multiprocessor task, Largest processing time first scheduling rule, Performance bound.

## 1. INTRODUCTION

An assumption in conventional scheduling problems $[1-3]$ is that each task is processed on only "one processor" at a time. The assumption was modified by Blazewicz, Drabowski, and Weglarz $[4,5]$ such that different tasks are to be processed on a "different number of processors." They considered a set $\mathbb{T}$ of $n$ independent tasks which are to be processed on a set of $m$ identical processors $P=\left\{P_{1}, P_{2}, \ldots, P_{m}\right\}$. This set of $n$ independent tasks, $\mathbb{T}$, can be decomposed into $k$ subsets, such that $\mathbb{T}=T^{1} \cup T^{2} \cup \cdots \cup T^{k}$ where $T^{1}=\left\{T_{1}^{1}, T_{2}^{1}, \ldots, T_{m_{1}}^{1}\right\}, T^{2}=\left\{T_{1}^{2}, T_{2}^{2}, \ldots, T_{n_{2}}^{2}\right\}$, $\ldots, T^{k}=\left\{T_{1}^{k}, T_{2}^{k}, \ldots, T_{n_{k}}^{k}\right\}$ and $n=n_{1}+n_{2}+\cdots+n_{k}$. That is, each task in $T^{1}$ requires one arbitrary processor for its processing and each task in $T^{j}$ requires $j$ arbitrary processors simultaneously for its processing. In summary, for a task $T_{i}^{j}, i=1,2, \ldots, n_{j}$, exactly $j$ arbitrary processors (here, $j$ can be 1 to $k$ ) are allocated to it during a period of $t_{i}^{j}$, which is the computation time of task $T_{i}^{j}$. Note that in the defined multiprocessor task scheduling problem, a schedule is feasible if each task $T_{i}^{j}$ can be exactly processed by $j$ processors simultaneously, and the performance of a feasible schedule is measured by its schedule length such that a feasible schedule is called an optimal schedule if it is of minimum schedule length.

In solving the above problem, polynomial time algorithms were proposed by Blazewicz, Drabowski, and Weglarz [5]. But these optimal algorithms were only designed for two constrained cases of independent multiprocessor task schedulings: one case was that multiprocessor tasks are with "unit" computation time; the other one was that multiprocessor tasks are with arbitrary computation time, but they need to be "preemptive."

In this paper, we will pay attention to the problem of scheduling independent "nonpreemptive" multiprocessor tasks with "arbitrary" computation times, which is NP-complete because the scheduling of the independent nonpreemptive tasks with arbitrary computation time of $T^{1}$, a special case of this problem, was already known to be NP-complete [1,3]. Therefore, we are interested in polynomial time approximation algorithms, that is, we try to get an approximate solution using heuristic algorithms. We say that a heuristic algorithm has a performance bound $\beta$ if $\left(S_{H} / S_{O}\right) \leq \beta$ for all problem instances, where $S_{H}$ and $S_{O}$ denote the heuristic schedule length and the optimal schedule length, respectively.

Remember that the Largest Processing Time first scheduling rule (LPT) was a well-known rule for the conventional scheduling of independent tasks nonpreemptively on identical processors. In other words, the LPT is a rule for scheduling those independent nonpreemptive tasks $T^{1}$ in a homogeneous system of processors. The principle of LPT is that whenever " $a$ " processor becomes free, select a task whose computation time is the largest of those not yet assigned tasks to assign to it. Graham analyzed [2] that the ratio of the LPT and the optimal schedule lengths is bounded by $\left(\frac{4}{3}-\frac{1}{3 m}\right)$, where $m$ is the number of processors in a system. Based on the principle of LPT, we develop a heuristic algorithm for the problem of scheduling independent nonpreemptive multiprocessor tasks and derive the performance bounds for two cases of this algorithm.

The remainder of this paper is organized as follows: The basic concept of Largest-ProcessingTime first scheduling rule is presented and based on this LPT rule, the algorithm and the performance bound of scheduling one set of independent nonpreemptive multiprocessor tasks, say $T^{j}$, are derived in Section 2. Section 3 illustrates how the derived algorithm can be extended to schedule the complete task set $\mathbb{T}$. The performance bound of this heuristic algorithm is proved to be bounded by $\left(\frac{4}{3} k-[k(k+1) / 6 m]\right)$ for the case in which both the number of processors in a system and the number of processors required by each task are arbitrary. Furthermore, the performance bound for a special case, in which both of the number of processors in a system and the number of processors required by each task are power of 2 , is shown to become ( $2-\frac{l}{m}$ ). Finally, some concluding remarks are given in Section 4.

## 2. THE LARGEST PROCESSING TIME FIRST SCHEDULING RULE

The largest processing time first scheduling rule (LPT) is a popular heuristic algorithm for the conventional nonpreemptive scheduling of independent tasks with arbitrary computation times. This LPT rule was first proposed by Graham in [2] to nonpreemptively schedule independent tasks which require only "one" processor whenever they are in processing. A formal description of LPT is given as follows:
Algorithm LPT.
Begin
Input task set $T^{1}$;
While (there exist tasks unassigned in $T^{1}$ ) do Begin

If (there exists a free processor $P_{\alpha}, \alpha=1,2, \cdots=1,2, \ldots, m$ ) then Begin

Assign task $T_{i}^{1}$ whose computation time is the largest among those tasks in $T^{1}$ to processor $P_{\alpha}$; Remove task $T_{i}^{1}$ from $T^{1}$;
Endif
EndWhile

## End

From the above algorithm, it is obvious that tasks have to be sorted in nonincreasing order of their computation times. Then, tasks are assigned, according to this sorting sequence, to the first free processor they meet. Graham has shown that the performance bound of LPT is $\left(\frac{4}{3}-\frac{1}{3 m}\right)$.

That is, let $S_{H}$ denote the schedule length of a corresponding LPT schedule and $S_{O}$ denote the schedule length of an optimal schedule. Then, $\left(S_{H} / S_{O}\right) \leq\left(\frac{4}{3}-\frac{1}{3 m}\right)$, where $m$ is the number of processors in a system. A simple example will be given to illustrate the above result.
Example 1. Assume that there are 7 tasks $T_{1}^{1}, T_{2}^{1}, T_{3}^{1}, T_{4}^{1}, T_{5}^{1}, T_{6}^{1}$, and $T_{7}^{1}$ submitted to a system of 3 identical processors. The computation times of these tasks are $5,5,4,4,3,3$, and 3 , respectively. Figures 1 (a) and (b) illustrate the LPT schedule and an optimal schedule, respectively. From the figure, we observe that $\frac{S_{H}}{S_{O}}=\frac{11}{9}=\frac{4}{3}-\frac{1}{3 \times 3}$.


Figure 1. The LPT schedule and an optimal schedule.
In the following, we will apply the concept of LPT [2] to the nonpreemptive scheduling of independent mutiprocessor task set $T^{j}$, where each task in $T^{j}$ requires exactly $j$ arbitrary processors for its processing, and derive its performance bound.

Theorem 1. The performance bound of scheduling one set of independent nonprecmptive multiprocessor tasks $T^{j}=\left\{T_{1}^{j}, T_{2}^{j}, \ldots, T_{n}^{j}, T_{n j}^{j}\right\}$, based on the rule of LPT, on $m$ identical processor systems is bounded by $\left(\frac{4}{3}-\frac{1}{3 c_{j}}\right)$, where $j \leq m$ and $c_{j}=\left\lfloor\frac{m}{j}\right\rfloor$.
Proof. Assume that the independent nonpreemptive multiprocessor tasks in $T^{j}=\left\{T_{1}^{j}, T_{2}^{j}, \ldots\right.$, $\left.T_{n_{j}}^{j}\right\}$ have been sorted in nonincreasing order of their computation times. By using the LPT rule, tasks in $T^{j}=\left\{T_{1}^{j}, T_{2}^{j}, \ldots, T_{n_{j}}^{j}\right\}$ are assigned to a system of $m$ identical processors. Let $S_{H}^{j}$ and $S_{O}^{j}$ denote the LPT schedule length and the optimal schedule length of the task subset $T^{j}$, respectively.

We can assume that $m=\left(c_{j} \times j\right)+r_{j}$, where $c_{j}$ and $r_{j}$ are positive integers. If $r_{j} \neq 0$, then the $m$-processor system can be seen as a ( $c_{j} \times j$ )-processor system, because the number of processors can be used by the set of task $T^{j}$ is $\left(c_{j} \times j\right)$ and there would always exist $r_{j}$ free processors. Thus, we can consider this system as a ( $c_{j} \times j$ )-processor or an ( $m-r_{j}$ )-processor system.
(1) If $S_{H}^{j}=S_{O}^{j}$, then it is obvious that the theorem holds.
(2) For the case that $S_{H}^{j}>S_{O}^{j}$, there exists a largest integer $x_{j}$, such that the $x_{j}$ longest tasks of the task set $T^{j}$ are assigned to ( $c_{j} \times j$ ) identical processors by the LPT scheduling rule with an optimal schedule. Then, if the longest computation time of the remaining ( $n_{j}-x_{j}$ ) unassigned tasks is $\tau=\max _{x_{j}<i \leq n_{j}}\left\{t_{i}^{j}\right\}$, where $t_{i}^{j}$ is the computation time of task $T_{i}^{j}$, it is obvious that the above ( $c_{j} \times j$ ) processors cannot be idle before time ( $S_{H}^{j}-\tau$ ).
Hence,

$$
\begin{equation*}
j \sum_{i=1}^{n_{j}} t_{i}^{j} \geq\left(m-r_{j}\right) \times\left(S_{H}^{j}-\tau\right)+(j \times \tau) . \tag{2.1}
\end{equation*}
$$

From equation (2.1), we have equation (2.2):

$$
\begin{equation*}
S_{O}^{j} \geq \frac{j}{\left(m-r_{j}\right)} \sum_{i=1}^{n_{j}} t_{i}^{j} \geq S_{H}^{j}-\left(\frac{m-r_{j}-j}{m-r_{j}}\right) \tau . \tag{2.2}
\end{equation*}
$$

With $\left(m-r_{j}\right)=\left(c_{j} \times j\right)$, and from equation (2.2), we derive equation (2.3):

$$
\begin{equation*}
S_{O}^{j} \geq S_{H}^{j}-\left(\frac{c_{j}-1}{c_{j}}\right) \tau \tag{2.3}
\end{equation*}
$$

Since there are at least $\left(x_{j}+1\right)$ tasks whose computation time are greater than or equal to $\tau$, the ( $m-r_{j}$ ) processors must be assigned with at least $\left(1+\left\lceil\frac{x_{j}}{c_{j}}\right\rceil\right)$ of these $\left(x_{j}+1\right)$ longest tasks. This implies:

$$
\begin{equation*}
S_{O}^{j} \geq\left(1+\left\lceil\frac{x_{j}}{c_{j}}\right\rceil\right) \tau \tag{2.4}
\end{equation*}
$$

From equation (2.3) and (2.4), we can derive equation (2.5) which is the performance bound equation of the task set $T^{j}$ on an $m$ identical processor system.

$$
\begin{equation*}
\frac{S_{H}^{j}}{S_{O}^{j}} \leq\left(1+\frac{1-\frac{1}{c_{j}}}{1+\left\lceil\frac{x_{j}}{c_{j}}\right\rceil}\right) \tag{2.5}
\end{equation*}
$$

Since $x_{j}$ must be greater than $c_{j}$ (otherwise it would be an optimal schedule), the worst case occurs when $\left[\left(x_{j}\right) /\left(c_{j}\right)\right\rceil=2$. Therefore, the worst performance bound is $\left(\frac{4}{3}-\frac{1}{3 c_{j}}\right)$.

The following simple example will show that the above performance bound is tight.
Example 2. Consider a system in which there are 5 identical processors, $m=5$, and a set of tasks $T^{2}=\left\{T_{1}^{2}, T_{2}^{2}, T_{3}^{2}, T_{4}^{2}, T_{5}^{2}\right\}$ are submitted to this system. The computation times of $T_{1}^{2}, T_{2}^{2}, T_{3}^{2}, T_{4}^{2}$, and $T_{5}^{2}$ are $3,3,2,2$, and 2 , respectively. The LPT schedule and an optimal schedule are illustrated in Figures 2 (a) and (b), respectively. Since $m=5$ and $j=2$, we can calculate that $c_{2}=\left\lfloor\frac{5}{2}\right\rfloor=2$. Thus, we conclude that the performance bound $\left(S_{H}^{2}\right) /\left(S_{O}^{2}\right)=\frac{7}{6}=\left(\frac{4}{3}-\frac{1}{3 \times 2}\right)$ is tight.


Figure 2. The LPT schedule and an optimal schedule of task set $T^{2}$.

## 3. HEURISTIC ALGORITHM

In this section, we will propose a heuristic algorithm for the problem of scheduling independent nonpreemptive multiprocessor tasks which is based on the concept of LPT rule. Before giving a formal description of our heuristic algorithm, the multiprocessor task scheduling problem is stated as follows.

Consider a set of $n$ independent nonpreemptive tasks, $\mathbb{T}$, to be processed on a set of $m$ identical processors $P=\left\{P_{1}, P_{2}, \ldots, P_{m}\right\}$. Suppose that this set of $n$ independent multiprocessor tasks can be decomposed into $k$ task subsets $T^{1}=\left\{T_{1}^{1}, T_{2}^{1}, \ldots, T_{n_{1}}^{1}\right\}, T^{2}=\left\{T_{1}^{2}, T_{2}^{2}, \ldots, T_{n_{2}}^{2}\right\}, \ldots$, $T^{k}=\left\{T_{1}^{k}, T_{2}^{k}, \ldots, T_{n_{k}}^{k}\right\}$ such that $\mathbb{T}=T^{1} \cup T^{2} \cup \cdots \cup T^{k}, n=n_{1}+n_{2}+\cdots+n_{k}$, and each task $T_{i}^{j}, i=1,2, \ldots, n_{j}$, requires " $j$ " arbitrary processors at a time for its processing during a period of time $t_{i}^{j}$, which is the computation time of task $T_{i}^{j}$. Here, tasks are said to be independent which means that there are no precedence constraints among them. Scheduling of tasks is termed nonpreemptive if a task cannot be interrupted once it has begun execution; that is, it must be
allowed to run to completion. The problem of scheduling independent nonpreemptive tasks is an NP-complete problem. In this section, we will devote our attention to developing a heuristic algorithm for such a problem and deriving the performance bounds for two cases of problems: one is that both the number of processors in systems and the number of processors required by each task are arbitrary; the other one is that both the number of processors in systems and the number of processors required by each task are power of 2 .

The formal description of our scheduling algorithm is illustrated below:

```
Heuristic Algorithm.
Begin
    Input task set T;
    Divide task set TT into k task subsets T}\mp@subsup{T}{}{1},\mp@subsup{T}{}{2},\ldots,\mathrm{ and T}\mp@subsup{T}{}{k}
    timefree =0 /* Initialize that processors were free at time 0. */
    For j=k downto 1 do
        Sort tasks in T}\mp@subsup{T}{}{j}\mathrm{ in nonincreasing order;
        Begin
            For i=1 to nj do /* Assume that there are nj tasks in Tj. */
            Begin
                While (there do not exist j free processors at time time free) do
                Begin
                        timeffree = timefree }+1
                EndWhile
                Assign task T}\mp@subsup{T}{i}{j}\mathrm{ to these j free processors;
            EndFor
        EndFor
End
```

In the above algorithm, the task set $\mathbb{T}$ is first divided into $k$ task subsets $T^{1}, T^{2}, \ldots$, and $T^{k}$. Then, the tasks in each task subset are scheduled according to the LPT scheduling rule. The major policy of our algorithm is that the more number of processors a task requires the earlier our scheduling algorithm takes it into consideration.

In the following theorems, we will show that the performance bounds of our heuristic algorithm for the above two cases of problems.

Theorem 2. The performance bound of our scheduling algorithms is bounded by $\left(\frac{4}{3} k-\frac{k(k+1)}{6 m}\right)$, in the case that the number of processors in a system and the number of processors required by each task are arbitrary.
Proof. Let $S_{H}$ and $S_{O}$ represent, respectively, the heuristic schedule length and the optimal schedule length of the task set $\mathbb{T}$. Since $S_{H}=S_{H}^{1} \cup S_{H}^{2} \cup \cdots \cup S_{H}^{k}$, where $S_{H}^{j}$ represents the schodule length of the task subset $T^{j}$ using the LPT scheduling rule on an $m$-processor system, for $j=1,2, \ldots, k$. Therefore,

$$
\begin{aligned}
& \frac{S_{H}}{S_{O}} \leq \frac{S_{H}^{1}+S_{H}^{2}+\cdots+S_{H}^{k}}{S_{O}} \\
\Rightarrow & \frac{S_{H}}{S_{O}} \leq \frac{S_{H}^{1}}{S_{O}}+\frac{S_{H}^{2}}{S_{O}}+\cdots+\frac{S_{H}^{k}}{S_{O}} \\
\Rightarrow & \frac{S_{H}}{S_{O}} \leq \frac{S_{H}^{1}}{S_{O}^{1}}+\frac{S_{H}^{2}}{S_{O}^{2}}+\cdots+\frac{S_{H}^{k}}{S_{O}^{k}},
\end{aligned}
$$

because $S_{O}^{j} \leq S_{O}$, where $S_{O}^{j}$ represents the optimal schedule length of any task subset $T^{j}$ on an $m$-processor system for $j=1,2, \ldots, k$.

$$
\begin{aligned}
& \Rightarrow \frac{S_{H}}{S_{O}} \leq \sum_{j=1}^{k}\left(\frac{4}{3}-\frac{1}{3 c_{j}}\right) \\
& \Rightarrow \frac{S_{H}}{S_{O}} \leq \sum_{j=1}^{k}\left(\frac{4}{3}-\frac{j}{3 m}\right), \quad \text { for } c_{j}=\left\lfloor\frac{m}{j}\right\rfloor \\
& \Rightarrow \frac{S_{H}}{S_{O}} \leq\left(\frac{4}{3} k-\frac{k(k+1)}{6 m}\right), \quad \text { where } k \leq m .
\end{aligned}
$$

In the following, we will consider the case in which both of the number of processors in a system and the number of processors required by each task are power of 2 . Such a case can be found in hypercube systems. The performance bound of our heuristic algorithm in this case is shown in Theorem 3.

Lemma 1. The number of free processors in a system would always be a multiple of the number of processors required by a task which is the next to be scheduled.
Proof. When both of the number of processors in a system and the number of processors required by each task are power of 2 , we can assume that the number of processors in the system is $2^{\log _{2} m}$ and the task sets are $T^{2^{h}}, T^{2^{h-1}}, \ldots, T^{1}$, where $h \leq \log _{2} m$. It is clear that if $\left\{2^{\log _{2} m}-\sum_{i=u}^{h}\left(a_{i} \times 2^{i}\right)\right\}>0$, then it must be a multiple of $2^{u}$, where $0 \leq u \leq h$, and $a_{i}$ is either a positive integer or a zero. The policy of our algorithm is the more number of processors a task requires, the earlier it will be scheduled. Thus, it is obvious that the lemma holds.
Lemma 2. If the task $T_{x}^{q}$ is finished at time $S_{H}$, then there would be no processors idle before ( $S_{H}-t_{x}^{q}$ ), where $q$ is an integer of power of 2 and $q \leq m, 1 \leq x \leq n_{q}, t_{x}^{q}$ is the computation time of task $T_{x}^{q}$, and $S_{H}$ is the schedule length of our heuristic algorithm.
Proof. We will prove this lemma by contradiction. By Lemma 1, if there were some processors idle before ( $S_{H}-t_{x}^{q}$ ), there must be enough free processors for the task $T_{x}^{q}$. Then, $T_{x}^{q}$ should be scheduled before ( $S_{H}-t_{x}^{q}$ ). This leads to a contradiction. Therefore, there would be no processor idle before ( $S_{H}-t_{x}^{q}$ ).

Theorem 3. The performance bound of our heuristic algorithm is bounded by $\left(2-\frac{1}{m}\right)$, if both the number of processors in a system and the number of processors required by each task are power of 2 , where $m$ is the number of processors in a system.
Proof. Assume that the task $T_{x}^{q}$ is finished at time $S_{H}$, where $q \leq k \leq m, x \leq n_{q}$, and $S_{H}$ is the schedule length of the task set $\mathbb{T}$ by our heuristic algorithm. By Lemma 2, we can get:

$$
\begin{aligned}
& \left(m \times S_{H}\right) \leq \sum_{j=1}^{k} \sum_{i=1}^{n_{j}}\left(j \times t_{i}^{j}\right)+(m-1) \times t_{x}^{q} \\
& \Rightarrow S_{H} \leq \frac{\sum_{j=1}^{k} \sum_{i=1}^{n_{j}}\left(j \times t_{i}^{j}\right)}{m}+\frac{(m-1)}{m} \times t_{x}^{q} .
\end{aligned}
$$

Since $\left(\left[\sum_{j=1}^{k} \sum_{i=1}^{n_{j}}\left(j \times t_{i}^{j}\right)\right] / m\right) \leq S_{O}$ and $t_{i}^{j} \leq S_{O}$, where $S_{O}$ denotes the optimal schedule length of the task set $\mathbb{T}$, for $j=1,2,4, \ldots, k, i=1,2, \ldots, n_{j}$.

$$
\begin{aligned}
& \Rightarrow S_{H} \leq S_{O}+\left(\frac{m-1}{m}\right) S_{O} \\
& \Rightarrow \frac{S_{H}}{S_{O}} \leq\left(2-\frac{1}{m}\right)
\end{aligned}
$$

We will give a simple example to show that the performance bound is almost tight.
Example 3. Let $t_{1}^{j}=m-2$ for $j=2^{1}, 2^{2}, 2^{3}, \ldots, 2^{\log _{2} m-1}, t_{i}^{2}=1$ for $i=1,2, \ldots, m-2, t_{1}^{1}=m$, and $t_{2}^{1}=m$. From Figure 3, we observe that the heuristic schedule length $S_{H}=(2 m-2)$ and an optimal schedule length $S_{O}=m$. Thus, $\left(S_{H} / S_{O}\right)=(2 m-2 / m)=\left(2-\frac{2}{m}\right)$.


Figure 3. The heuristic schedule and an optimal schedule.

## 4. CONCLUSIONS

In this paper, we propose a heuristics algorithm for the problem of scheduling independent nonpreemptive multiprocessor tasks and show that its performance bound is bounded by $\left(\frac{4}{3} k-[k(k+1) / 6 m]\right)$ in case the number of processors in the system and the number of processors required by each task are arbitrary. Besides, we also derive that the performance bound of our scheduling algorithm is bounded by ( $2-\frac{1}{m}$ ), an almost tight performance bound, if both of the number of processors in a system and the number of processors required by each task are power of 2 .

Since the performance bound of scheduling nonpreemptive multiprocessor tasks for the case in which the number of processors in a system and the number of processors required by each task are arbitrary is not tight, our future research is to find a tight performance bound. Considering the case in which the precedence constraints among tasks is included will be our next research topic.

## REFERENCES

1. E.G. Coffman, Jr., Ed., Computer and Job-Shop Scheduling Theory, John Wiley, New York, (1976).
2. R.L. Graham, Bounds on Multiprocessing Timing Anomalies, SIAM J. Appl. Math. 17, 416-429 (1969).
3. J.D. Ullman, NP-complete scheduling problem, Journal of Computer and System Sciences 10, 384-393 (1975).
4. J. Blazewicz, M. Drabowski, and J. Weglarz, Scheduling independent 2-processor tasks to minimize schedule length, Information Processing Letter 18, 267-273 (1984).
5. J. Blazewicz, M. Drabowski, and J. Weglarz, Scheduling multiprocessor tasks to minimize schedule length, IEEE Trans. on Computers C-35 (5), 389-393 (1986).
