2005 Deliverable Report: Supply Chain Quadratic Goal Programming Models and Solutions

Task 1214.002: Robust Configuration and Monitoring of Semiconductor Supply Chains Contract 1214: Configuration, Monitoring and Control of Semiconductor Supply Chains

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1. Abstract and Summary

This report presents a novel approach to formulating and optimizing the semiconductor supply chain performance. Unlike conventional modeling of supply chain operations, empirical quadratic models are established to describe the supply chain behavior. Quadratic goal programming model is then used to optimize the supply chain performance. In order to construct the quadratic response-surface models of supply chain performance, a supply chain simulation model is first set up. Simulation runs are then conducted based on a D-optimum experimental design. By the results of simulation experiments, quadratic models for different QoS priorities are built with stepwise regression. The quadratic response surfaces then serve as the objective functions of the goal programming model. Optimization is finally performed to obtain a robust supply chain configuration with its X-factors and cycle-time variability minimized.

2. Technical Results

2.1Semiconductor Supply Chain Simulation

In this study, a four-tier semiconductor supply chain is simulated. The first tier is Fabrication (Fab); the second is Circuit Prob. (CP): the third is Assembly (Ass); and the fourth is Final Test (FT). There are six fabrication facilities and their corresponding capacities are designed to mimic 200mm foundry fabs of renowned companies, such as TSMC, and UMC (Table 1). Each of the CP, the ASS and the FT tier has two facilities available to the supply chain. There are nine available supply chain routes (Fig. 1) to serve three products, A, B and C. However, not all the available routes are usable to each type of the products (Table 2). Since the supply chain simulation model is an aggregated model, the most challenging task is to estimate the cycle time of a facility, especially of the fabrication facility, which usually consists of more than 300 processing steps. The cycle time can be divided into two portions: processing time and queue time. The processing times vary with the product type and the facility. The queue times are assumed to follow log-normal distributions with means varying with the capacity utilization and the QoS priority. Finally, the mean of the log-normal distribution is determined such that the mean cycle time appears to be an exponential function of the capacity utilization (Fig. 2). The simulation model is built using EM Plant.

2.2 Quadratic Response Surface Models

There are two types of input variables and two types of response variables for the supply chain response surface models. The input variables are also the decision variables for the goal programming. The first type of input variables is the route mix variable, ρ_{kr} . Route mix ρ_{kr} represents the proportion of product k (k=1 for Product A, 2 for Product B and 3 for Product C) demand to be allocated to supply chain route r. The second type of input variables is the priority mix variable, π_{kq} . Priority mix π_{ka} denotes the proportion of product k demand to be produced in QoS priority level q. In this study, there are three QoS priority levels: super hot lots (q=1), hot lots (q=2) and normal lots (q=3). The two types of response variables are to be used to evaluate the entire supply chain's performance. The first type of variables is to measures the X-factor performance for each priority level. The second type of response variables is to measure the cycle time variability, i.e. cycle time standard deviation (CT-STD) in month. 180 runs of simulations chosen by D-optimum design are performed to obtain how the response variables are affected by the input variables. Stepwise regression is then used to select input variable terms, including linear, quadratic and interaction terms, into the response surface model. The resulting response surface models for QoS priority 1 are shown below.

X-factor₁=

3.04028 - 0.76932* ρ 18 - 5.32407* π 11* ρ 18 - 1.70306* ρ 22 + 2.55627* π 11* ρ 22 -

3.21206* \wp 24 + 2.0617* π 11* \wp 24 + 3.35593* \wp 22* \wp 24 + 6.45807* \wp 24^2 -

6.01907* ρ 26 + 2.64128* π 11* ρ 26 + 2.43013* ρ 18* ρ 26 + 3.95107* ρ 22* ρ 26 +

 $3.59718*\rho 24*\rho 26 + 6.27775*\rho 26^2 - 2.08025*\rho 24*\rho 33 + 0.95507*\rho 33^2 -$

0.89682* ρ 22* π 12 - 2.73278* ρ 22* π 21 - 2.94563* ρ 24* π 21 + 4.33951* π 11* π 31

+2.20954* ρ 22* π 31 + 2.26427* ρ 26* π 31 - 3.90953* ρ 33* π 31 +3.88601* π 11* π 32 -

0.69994* ρ 33* π 32 - 3.96577* π 11* ρ 11 - 1.57582* ρ 22* ρ 14 - 1.4345* ρ 15 -

6.01892* π 11* ρ 15 + 4.19858* ρ 26* ρ 15 + 6.76398* π 21* ρ 15

$CT-STD_1 =$

10.76424 - 30.39664* ρ 18 + 20.91593
* ρ 17* ρ 18 + 44.38031
* ρ 18^2 - 7.04376* ρ 22 +

3.75327*
 π 11* ρ 22 - 19.49143* ρ 17
* ρ 22 - 14.41669* ρ 24 - 19.90259
* ρ 17* ρ 24 +

17.13541* ρ 22
* ρ 24 + 19.52764* ρ 24^2 - 16.44176
* ρ 26 - 20.93092* ρ 17
* ρ 26 +

15.80926* ρ 22* ρ 26 + 16.97536* ρ 24* ρ 26 + 23.21307* ρ 26^2 + 5.01638* ρ 22* π 22 -

6.09423* π 31 + 9.03262* ρ 22* π 31 + 9.70458* ρ 24* π 31 + 7.29345* π 12* π 31 -

 $\begin{array}{l} 16.26179^{*}\,\rho\,11\,+\,18.03107^{*}\,\rho\,17^{*}\,\rho\,11\,+\,27.58228^{*}\,\rho\\ 18^{*}\,\rho\,11\,-\,5.73555^{*}\,\pi\,12^{*}\,\rho\,11\,+\,28.52365^{*}\,\rho\,17^{*}\,\rho\,14\\ +\,29.47225^{*}\,\rho\,18^{*}\,\rho\,14\,-\,5.43344^{*}\,\rho\,22^{*}\,\rho\,14\,+\\ 23.20821^{*}\,\rho\,11^{*}\,\rho\,14\,-\,55.31215^{*}\,\rho\,14^{*}2\,-\,17.66627^{*}\,\rho\\ 15\,+\,17.71866^{*}\,\rho\,17^{*}\,\rho\,15\,+\,22.28432^{*}\,\rho\,18^{*}\,\rho\,15\,+\\ 11.24435^{*}\,\rho\,26^{*}\,\rho\,15\,-\,5.38951^{*}\,\pi\,22^{*}\,\rho\,15\,+\\ 32.58414^{*}\,\rho\,11^{*}\,\rho\,15\,+25.65426^{*}\,\rho\,14^{*}\,\rho\,15 \end{array}$

2.3 Quadratic Goal Programming Models

With the quadratic response surfaces to describe how the supply chain performance responds to changes of supply chain configurations, the response surfaces can be used as the goal functions in the goal programming. The goal objective function is then:

 $\sum_{i=1}^{3} w_i$ (X-factor_i + CT-STD_i)

where w_i is the weight for priority *i* products; X-factor_{*i*} is the X-factor response surface for priority *i* and CT-STD_{*i*} is the CT-STD response surface for priority *i*. The constraints are listed below.

1. Product mix constraint:

 $\sum_{k} p_k = 1$

where p_k is the proportion of product k demand in the total demand and is given.

2. Priority mix constraints: $\sum p_k \cdot \pi_{kq} \leq \phi_q \quad \forall q$

$$\sum_{k} p_k \cdot \pi_{kq} \leq \varphi_q$$

$$\sum_{q} \pi_{kq} = 1 \quad \forall k$$

where ϕ_q is a preset upper limit for priority *q* proportion. 3. Route mix constraints:

$$\sum_{k} p_{k} \cdot \rho_{rk} \leq \alpha_{r} \quad \forall$$
$$\sum_{r} \rho_{rk} = 1 \quad \forall k$$

where α_r is a preset upper limit for the proportion of total demand to go through route r.

4. Capacity constraints:

$$E_t \cdot \sum_{r:\phi \in r} \sum_k (p_k \cdot \rho_{rk}) \cdot \frac{PT_{kt\phi}}{PT_{t\phi}} \le C_{t\phi} \quad \forall t, \phi$$

DT

where

- E_t is capacity utilization of supply chain tier t (an economy factor);
- $C_{t\phi}$ is proportion of facility f capacity in supply chain tier t;
- $PT_{t\phi}$ is average bottleneck operation processing time by facility f in supply chain tier t; and
- $PT_{kt\phi}$ is average bottleneck operation processing time of product k by facility f in supply chain tier t.

2.4 Supply Chain Optimization and Validation

With the quadratic goal programming model, optimization is performed using LINDO. Since the quadratic goal programming model has only linear constraints, the optimum solution found is ensured to be the global optimum. The results are shown in Table 3. The optimum supply chain route mix and priority mix settings are then validated through simulation and compared to the 180 simulation experiments in Table 4.

Table 1: Fab Capacity in Simulation Model

FAB	Capacity	
FAB1	1468k	
FAB2	1376k	• Capacity in 200mm wafers per year
FAB3	922k	• Fab1: 15MC 3, 0, 01 8 • Fab2:UMC 8C, 8D, 8E, or 8F
FAB4	1133k	 Fab3:TSMC 2 Fab4:TSMC 3, 4, or 7
FAB5	1202k	• Fab5:UMC 6A, or 8AB
FAB6	689k	¹ ■ Fado: water tech, vis, or SSMC
Total	6465 k	



Fig. 1 Semiconductor Supply Chain Routes

Product	Routes				
Α	145789				
В	2467				
С	3 4				

Table 2: Usable Routes for Products



Fig. 2 Mean Cycle Time vs. Capacity Utilization

Table 3 Optimum Supply Chain Configuration											
		Priority Mix		Product 1		Product 2		Product 3			
		Priority 1 Priority 2 Priority 3		0.05 0.1 0.85		0.05 0.25 0.7		0 0 (.15 .25).6		
Route Mix	Rou	te 1 Route		4 Route		e 5	Route 7		Route 8		Route 9
Product 1	0	0.2 0.1			0.114994		0.147	0.147153		3181	0.254671
	Rout	e Mix	Route	2 2	Route	e 4	Rout	e 6	Ro	ute 7	
Product 2		uct 2	0.1		0.2358	856	0.3	}	0.36	64144	
	Route Mi		1	Route 3		Route 4					
Pı		roduct 3		0.5292	286	0.4	47071	4			

X fa	X factor Priority1		Priority2	Priority3	
180 Runs of	MIN	1.40331	1.47294	1.703227	
Simulation Experiments	AVG	1.5897	1.8307	2.12257	
	MAX	2.19528	2.19381	2.338799	
Goal Prog Re	gramming esults	1.396099	1.566981	2.128181	
Simulation Validation of Goal Programming		1.3957	1.632579	2.078058	
CT-STD	(month)	Priority1	Priority2	Priority3	
180 Runs of Simulation Experiments	MIN	0.48536	0.81575	1.24547	
	AVG	1.09511	1.43061	1.8152	
	MAX	3.53199	2.48982	2.60458	
Goal Programming Results		0.3891889	0.2292796	1.4254032	
Simulation Validation of Goal Programming		0.486637	0.748724	1.20554	

Table 4: Validation of Optimized Supply Chain Performance